



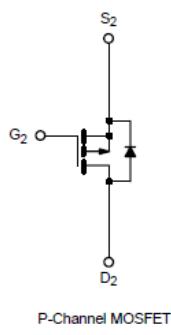
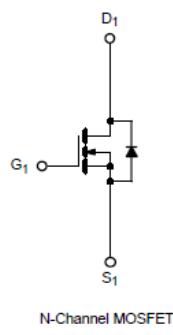
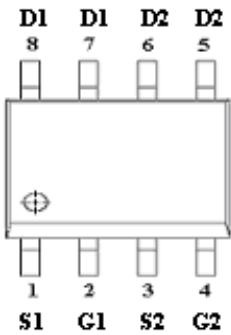
**Alfa-MOS
Technology**

**AFC4569WS
60V N & P Pair
Enhancement Mode MOSFET**

General Description

AFC4569WS, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent RDS(ON), low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Pin Description (SOP-8P)



Application

- Low Current DC/DC Conversion
- Load Switch
- CCFL Inverter
- Power Management in Notebook Computer

Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC4569WSS8RG	4569WS	SOP-8P	Tape & Reel	2500 EA

※ A Lot code

※ B Date code

※ AFC4569WSS8RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



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Absolute Maximum Ratings (N-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate –Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	8	A
		6	
Pulsed Drain Current	I_{DM}	20	A
Continuous Source Current(Diode Conduction)	I_S	1.5	A
Power Dissipation	P_D	2.8	W
		1.8	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics (N-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		2.0	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=48\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=48\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			5	
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS}\geq 5\text{V}, V_{GS}=10\text{V}$	20			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=8\text{A}$		28	36	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=6\text{A}$		32	40	
Forward Transconductance	g_{FS}	$V_{DS}=15\text{V}, I_D=15\text{A}$		20		S
Diode Forward Voltage	V_{SD}	$I_S=2.0\text{A}, V_{GS}=0\text{V}$		0.8	1.3	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=30\text{V}, V_{GS}=4.5\text{V}$ $I_D=8\text{A}$		6.5	13	nC
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			3		
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		680		pF
Output Capacitance	C_{oss}			150		
Reverse Transfer Capacitance	C_{rss}			60		
Turn-On Time	$t_{d(\text{on})}$	$V_{DD}=30\text{V}, R_L=1.3\Omega$ $I_D=8\text{A}, V_{GEN}=10\text{V}$		8	15	ns
	t_r			15	30	
Turn-Off Time	$t_{d(\text{off})}$			30	60	
	t_f			25	50	



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Absolute Maximum Ratings (P-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-60	V
Gate -Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	-8	A
		-6	
Pulsed Drain Current	I_{DM}	-20	A
Continuous Source Current(Diode Conduction)	I_S	-1.7	A
Power Dissipation	P_D	2.8	W
		1.8	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics (P-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

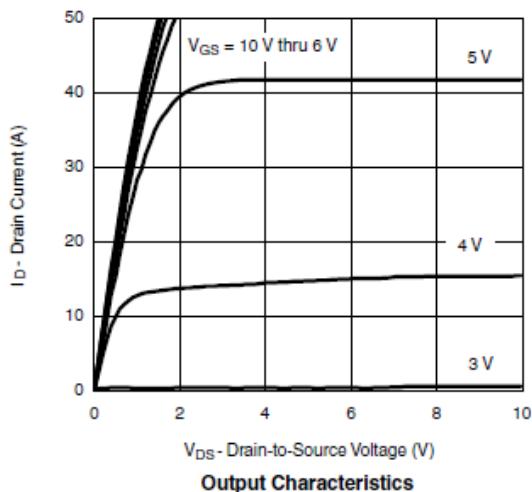
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D = -250\mu\text{A}$	-60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D = -250\mu\text{A}$	-1.0		-2.5	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -48\text{V}, V_{GS}=0\text{V}$			-1	uA
		$V_{DS} = -48\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			-20	
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS} \geq -5\text{V}, V_{GS} = -10\text{V}$	-20			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -8\text{A}$		52	60	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}, I_D = -6\text{A}$		61	72	
Forward Transconductance	g_{FS}	$V_{DS} = -15\text{V}, I_D = -3.2\text{A}$		12		S
Diode Forward Voltage	V_{SD}	$I_S = -3\text{A}, V_{GS}=0\text{V}$		-0.8	-1.3	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=-30\text{V}, V_{GS}=-10\text{V}$ $I_D = -8\text{A}$		25	40	nC
Gate-Source Charge	Q_{gs}			5		
Gate-Drain Charge	Q_{gd}			8		
Input Capacitance	C_{iss}	$V_{DS}=-25\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		1200	2000	pF
Output Capacitance	C_{oss}			140		
Reverse Transfer Capacitance	C_{rss}			90		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-30\text{V}, R_L=3.0\Omega$ $I_D=-8\text{A}, V_{GEN}=-10\text{V}$ $R_G=2.5\Omega$		10	20	ns
	t_r			10	20	
Turn-Off Time	$t_{d(off)}$			45	80	
	t_f			25	40	



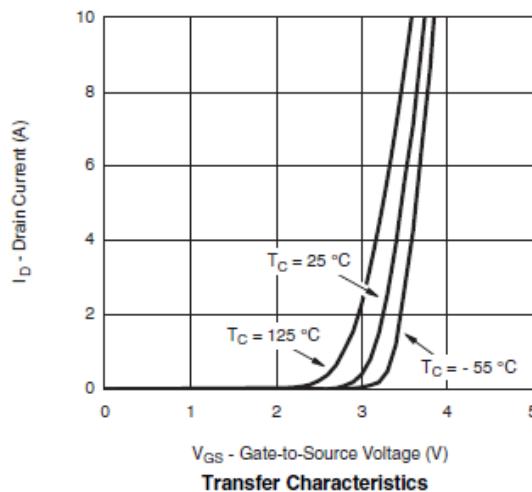
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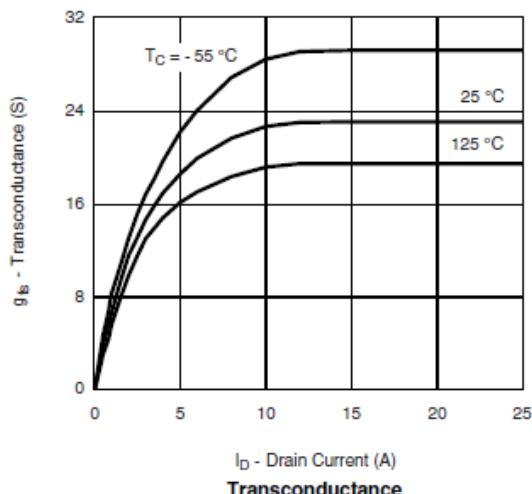
Typical Characteristics (N-Channel)



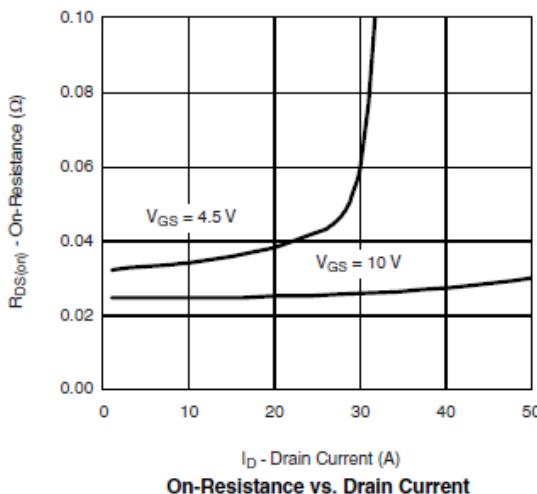
Output Characteristics



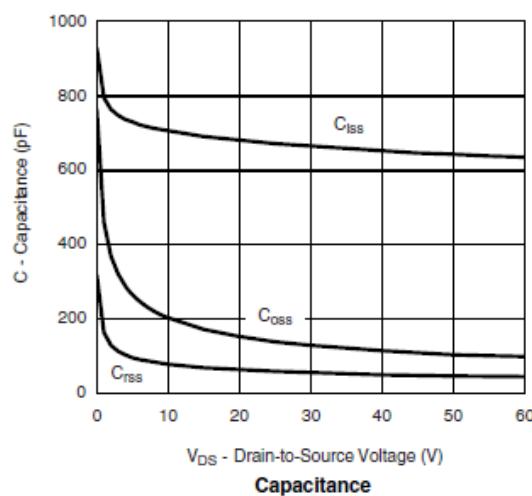
Transfer Characteristics



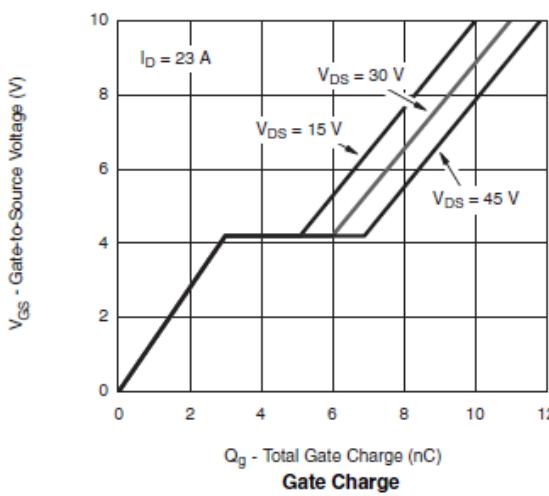
Transconductance



On-Resistance vs. Drain Current



Capacitance



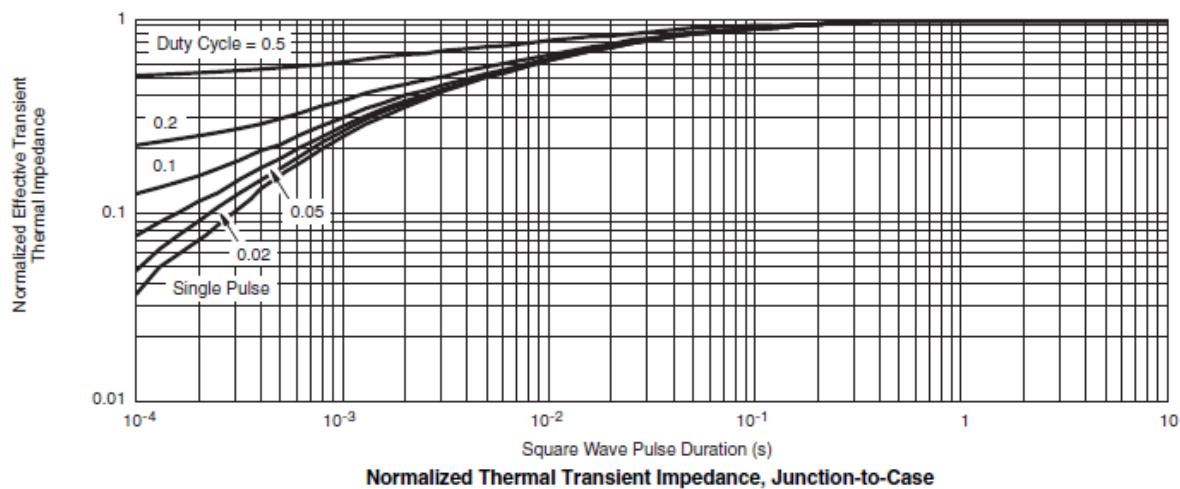
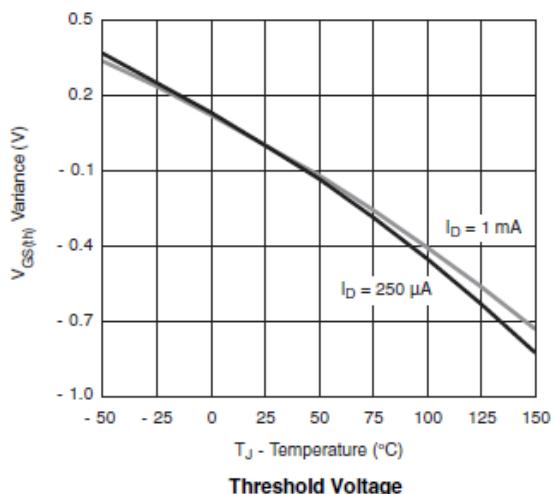
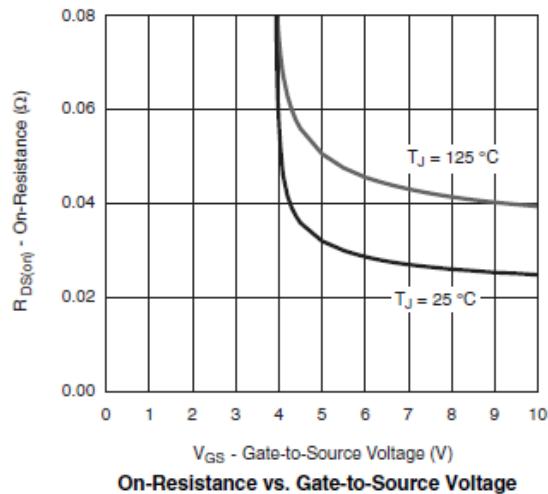
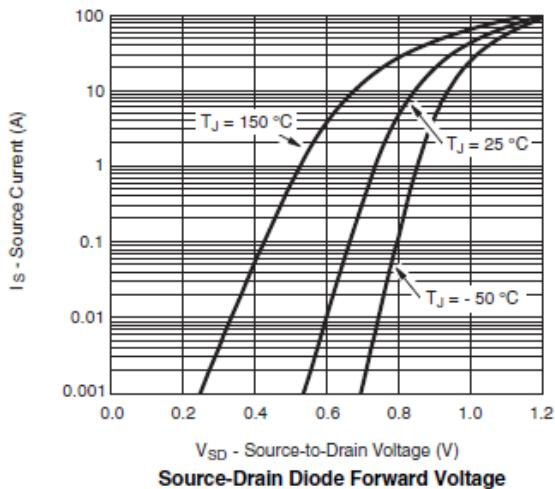
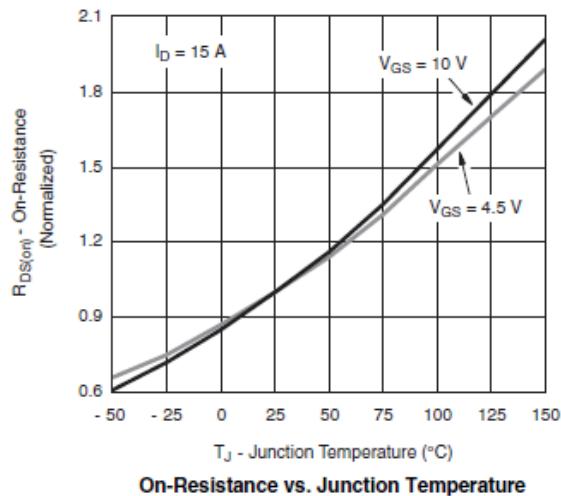
Gate Charge



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Typical Characteristics (N-Channel)

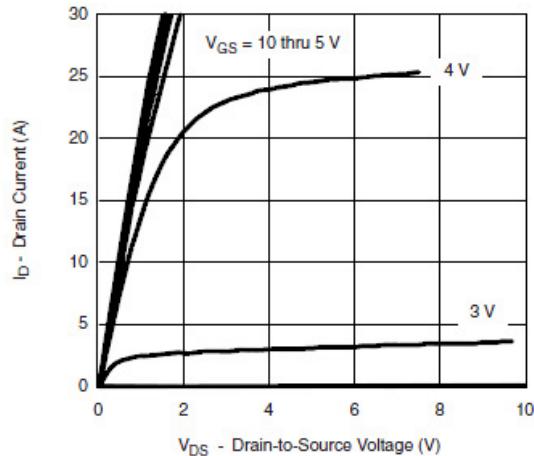




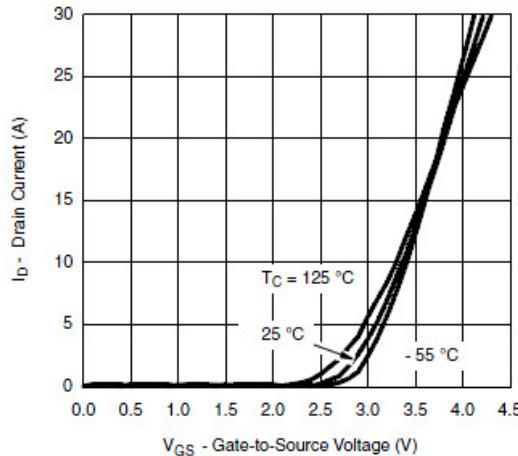
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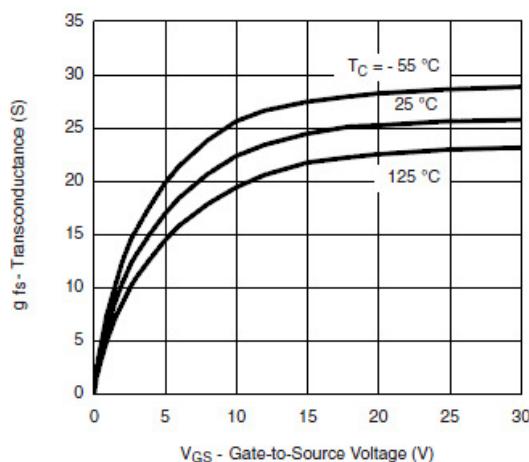
Typical Characteristics (P-Channel)



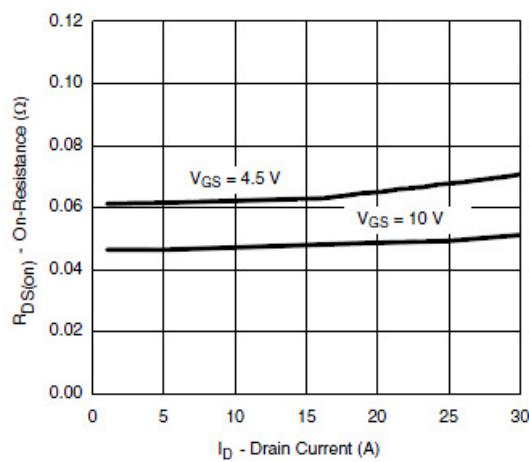
Output Characteristics



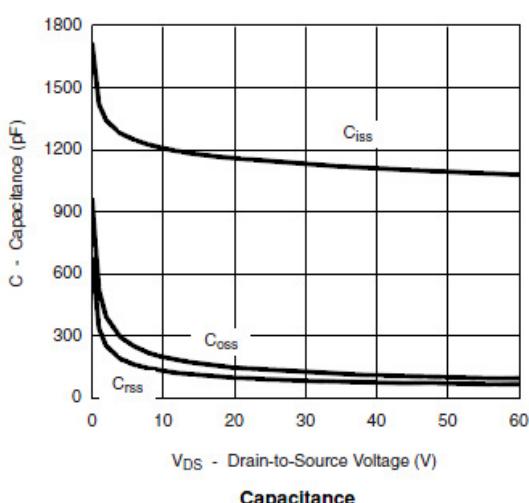
Transfer Characteristics



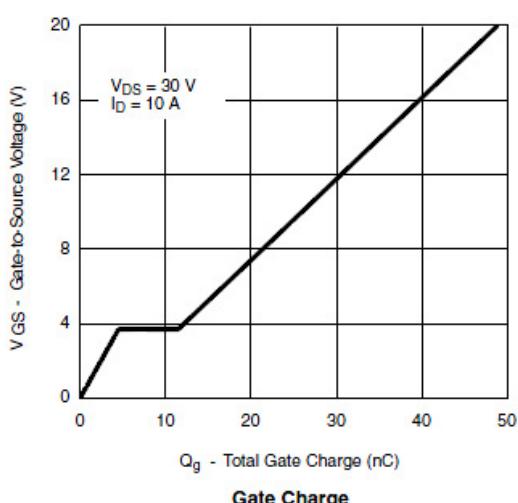
Transconductance



On-Resistance vs. Drain Current



Capacitance



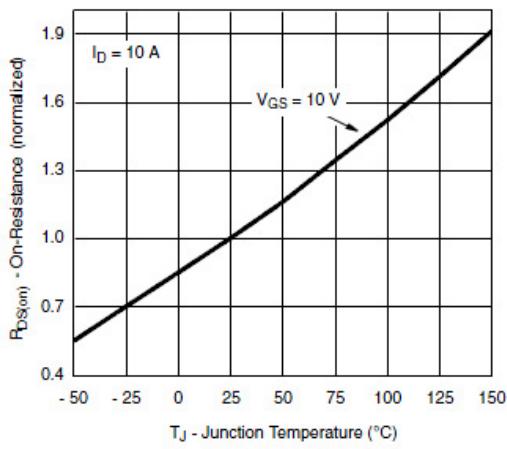
Gate Charge



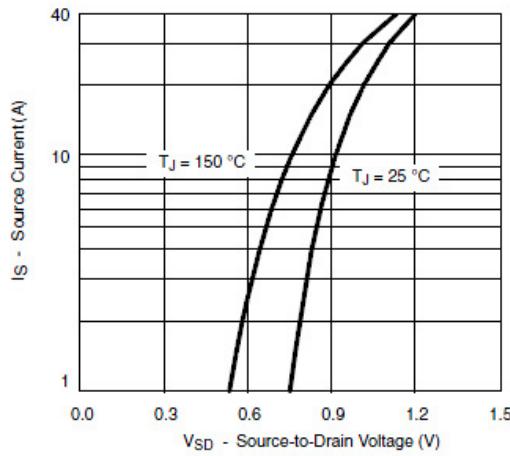
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Typical Characteristics (P-Channel)

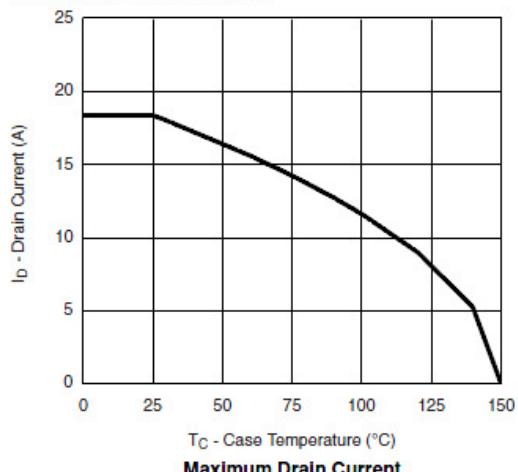


On-Resistance vs. Junction Temperature

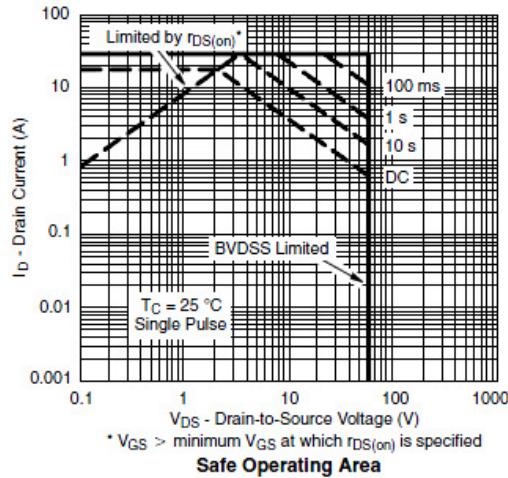


Source-Drain Diode Forward Voltage

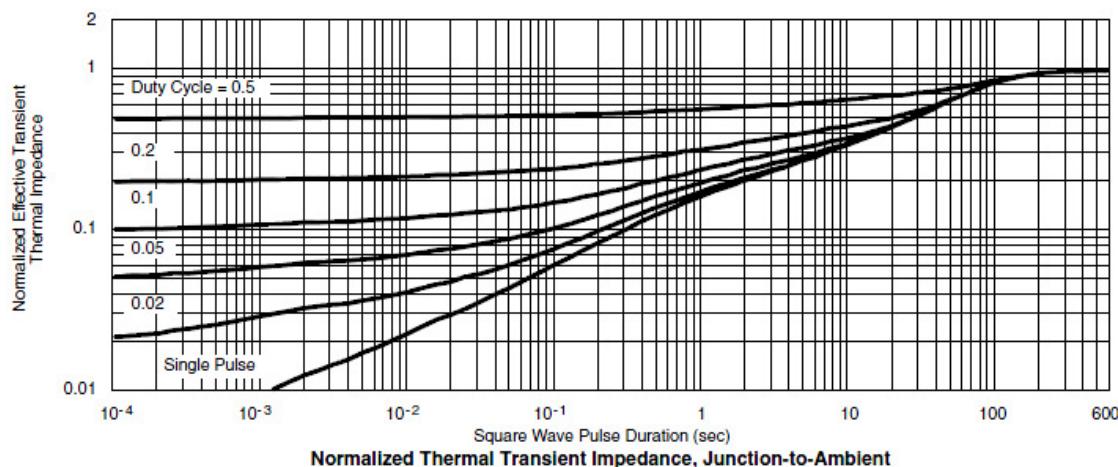
Thermal Ratings



Maximum Drain Current
vs. Case Temperature



Safe Operating Area



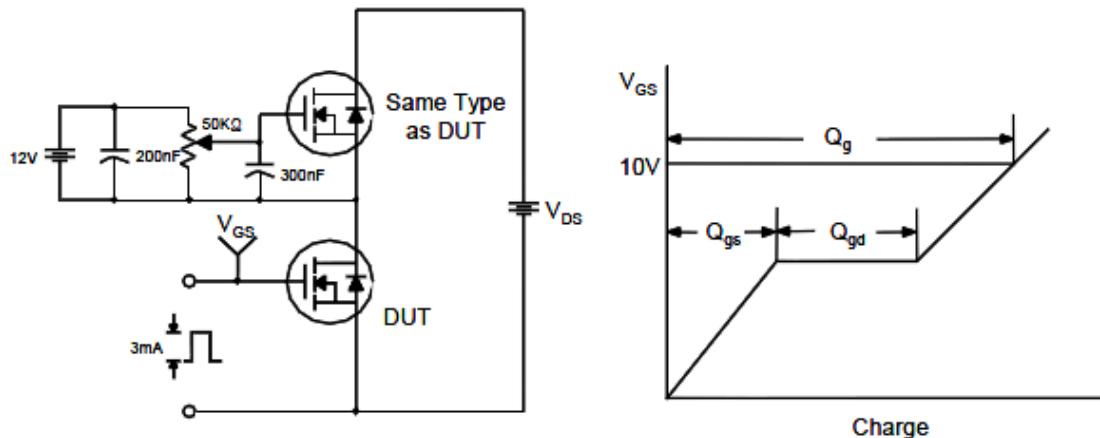


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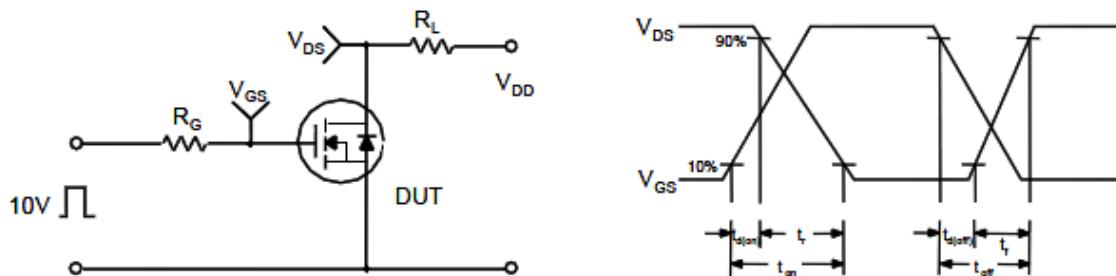
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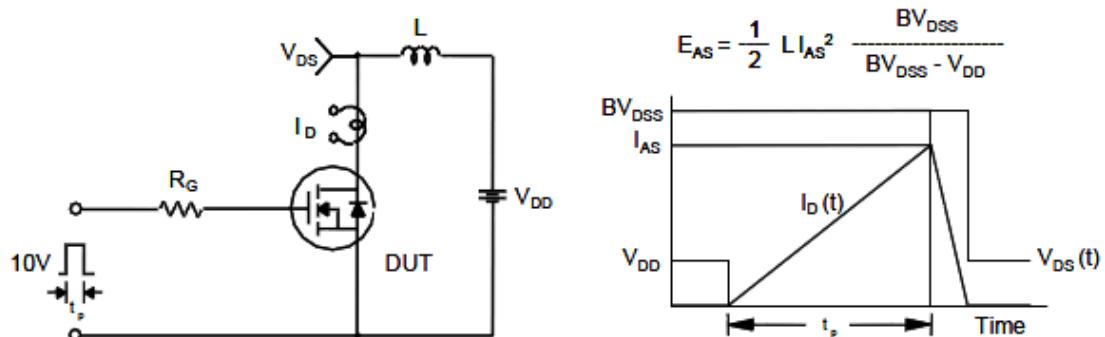
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

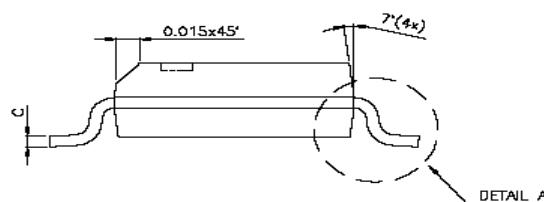
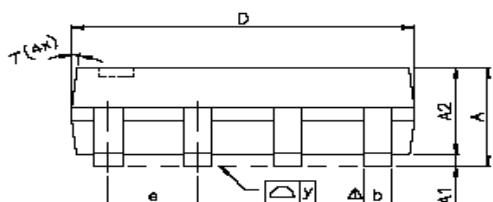
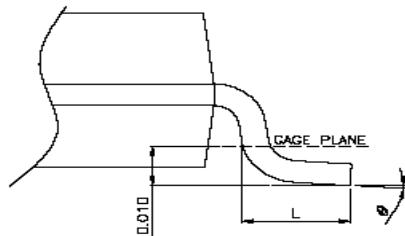
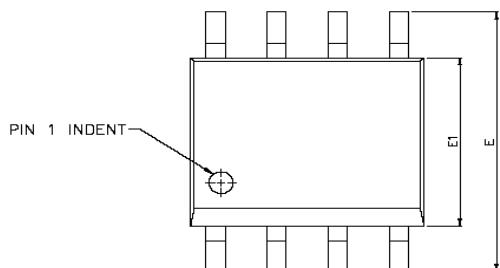




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Package Information (SOP-8P)



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
\triangle y	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°

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