



**Alfa-MOS
Technology**

**AFC5616
60V N & P Pair
Enhancement Mode MOSFET**

General Description

AFC5616, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent RDS(ON), low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

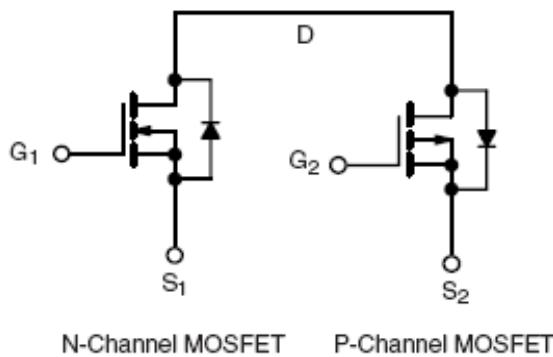
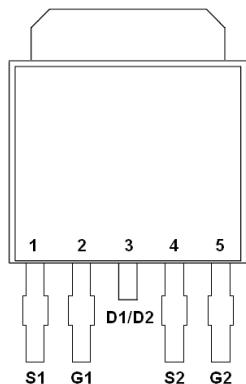
N-Channel

- 60V/7.0A, $R_{DS(ON)}=34m\Omega$ @ $V_{GS}=10V$
- 60V/6.0A, $R_{DS(ON)}=40m\Omega$ @ $V_{GS}=4.5V$

P-Channel

- -60V/-7A, $R_{DS(ON)}=56m\Omega$ @ $V_{GS}=-10V$
- -60V/-6A, $R_{DS(ON)}=68m\Omega$ @ $V_{GS}=-4.5V$

Pin Description (TO-252-4L)



Application

- DC/DC Conversion
- Load Switch
- DC FAN

Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D1 / D2	Drain 1 / Drain 2
4	S2	Source 2
5	G2	Gate 2

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC5616T254RG	5616	TO-252-4L	Tape & Reel	2500 EA

※ A Lot code

※ B Date code

※ AFC5616T254RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings (N-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate –Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	7.0	A
		6.0	
Pulsed Drain Current	I_{DM}	30	A
Continuous Source Current(Diode Conduction)	I_S	1.5	A
Power Dissipation	P_D	2.8	W
		1.8	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics (N-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		2.5	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=60\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			5	
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS}\geq 5\text{V}, V_{GS}=4.5\text{V}$	30			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=7.0\text{A}$		25	34	m Ω
		$V_{GS}=4.5\text{V}, I_D=6.0\text{A}$		30	40	
Forward Transconductance	g_{FS}	$V_{DS}=15\text{V}, I_D=5.3\text{A}$		24		S
Diode Forward Voltage	V_{SD}	$I_S=2.0\text{A}, V_{GS}=0\text{V}$		0.8	1.3	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=30\text{V}, V_{GS}=4.5\text{V}$ $I_D=23\text{A}$		7	15	nC
Gate-Source Charge	Q_{gs}			3.2		
Gate-Drain Charge	Q_{gd}			3.2		
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		700		pF
Output Capacitance	C_{oss}			150		
Reverse Transfer Capacitance	C_{rss}			70		
Turn-On Time	$t_{d(on)}$	$V_{DD}=30\text{V}, R_L=1.3\Omega$ $I_D=23\text{A}, V_{GEN}=10\text{V}$ $R_G=1\Omega$		10	20	ns
	t_r			15	30	
Turn-Off Time	$t_{d(off)}$			30	65	
	t_f			25	50	



Absolute Maximum Ratings (P-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-60	V
Gate -Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	-7.0	A
		-6.0	
Pulsed Drain Current	I_{DM}	-30	A
Continuous Source Current(Diode Conduction)	I_S	-1.7	A
Power Dissipation	P_D	2.8	W
		1.8	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics (P-Channel)

($T_A=25^\circ\text{C}$ Unless otherwise noted)

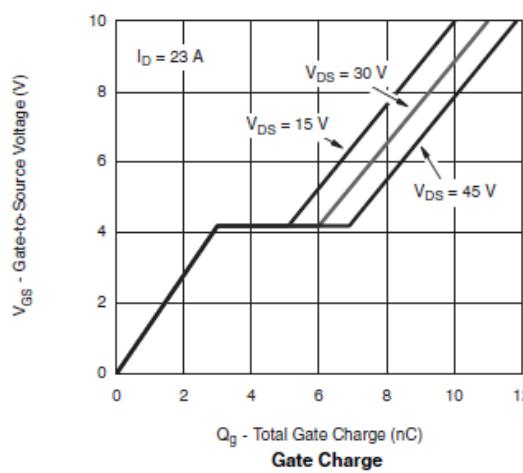
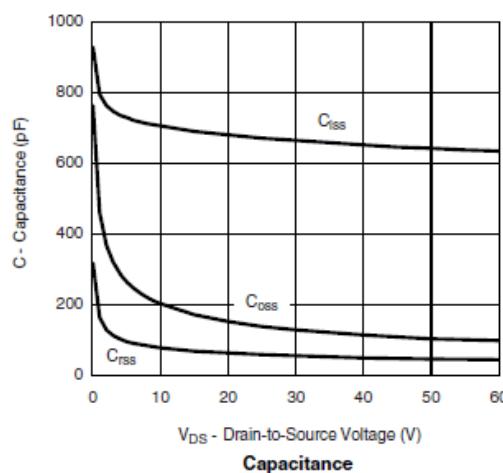
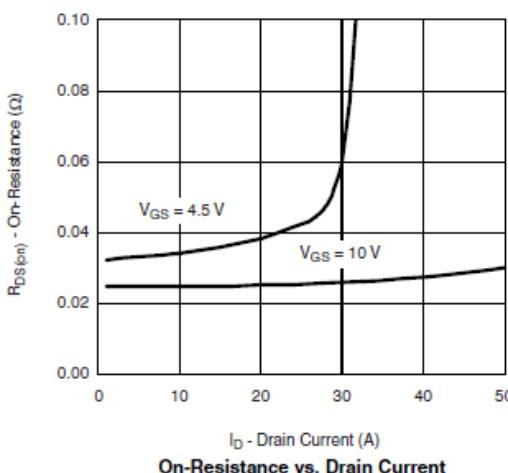
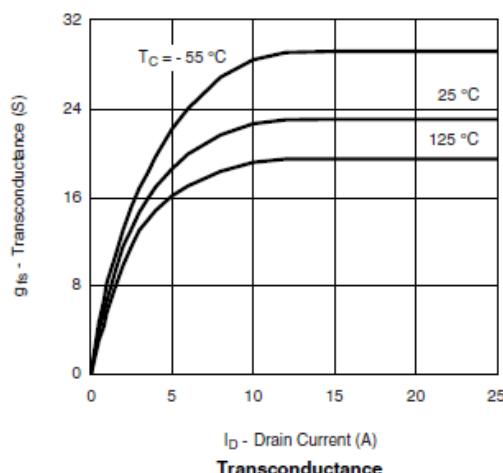
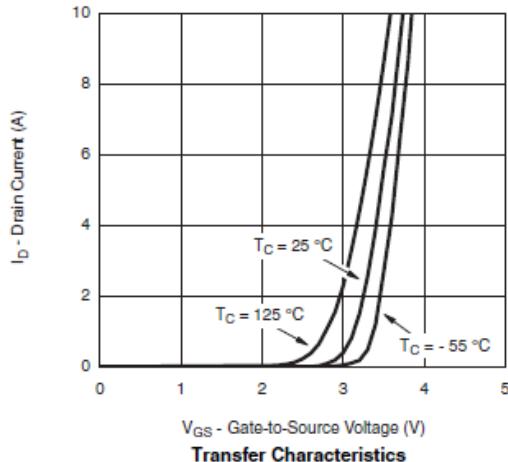
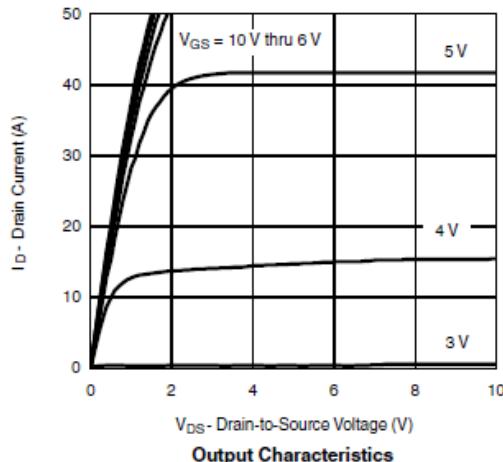
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D = -250\mu\text{A}$	-60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D = -250\mu\text{A}$	-1.0		-2.5	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -48\text{V}, V_{GS}=0\text{V}$			-1	
		$V_{DS} = -48\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			-20	uA
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS} \geq -5\text{V}, V_{GS} = -10\text{V}$	-30			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = -10\text{V}, I_D = -7\text{A}$	46	56		m Ω
		$V_{GS} = -4.5\text{V}, I_D = -6\text{A}$	56	68		
Forward Transconductance	g_{FS}	$V_{DS} = -15\text{V}, I_D = -3.2\text{A}$		12		S
Diode Forward Voltage	V_{SD}	$I_S = -3\text{A}, V_{GS}=0\text{V}$		-0.8	-1.3	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=-30\text{V}, V_{GS}=-10\text{V}$		25	40	nC
Gate-Source Charge	Q_{gs}			5		
Gate-Drain Charge	Q_{gd}			8		
Input Capacitance	C_{iss}	$V_{DS}=-25\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		1200	2000	pF
Output Capacitance	C_{oss}			140		
Reverse Transfer Capacitance	C_{rss}			90		
Turn-On Time	$t_{d(\text{on})}$	$V_{DD}=-30\text{V}, R_L=3.0\Omega$		10	20	ns
	t_r			10	20	
Turn-Off Time	$t_{d(\text{off})}$			45	80	
	t_f			25	40	



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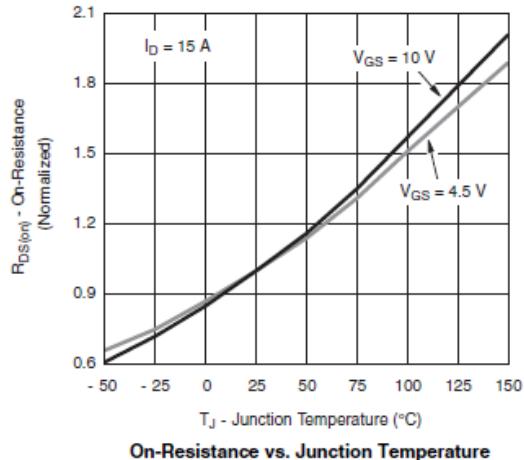
**AFC5616
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Typical Characteristics (N-Channel)

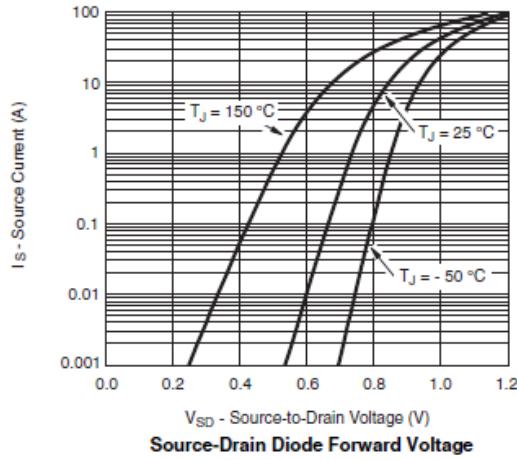




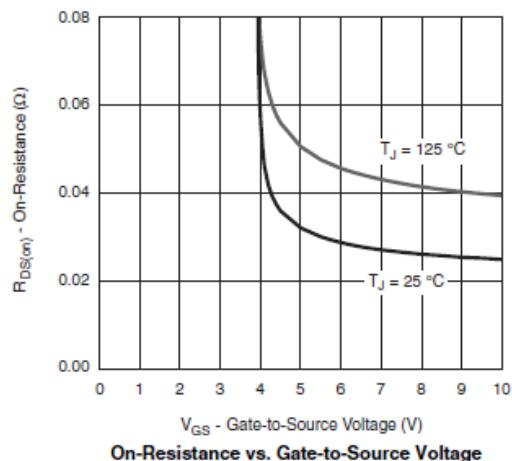
Typical Characteristics (N-Channel)



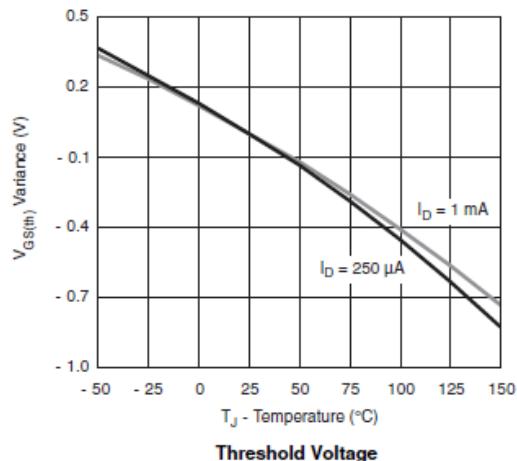
On-Resistance vs. Junction Temperature



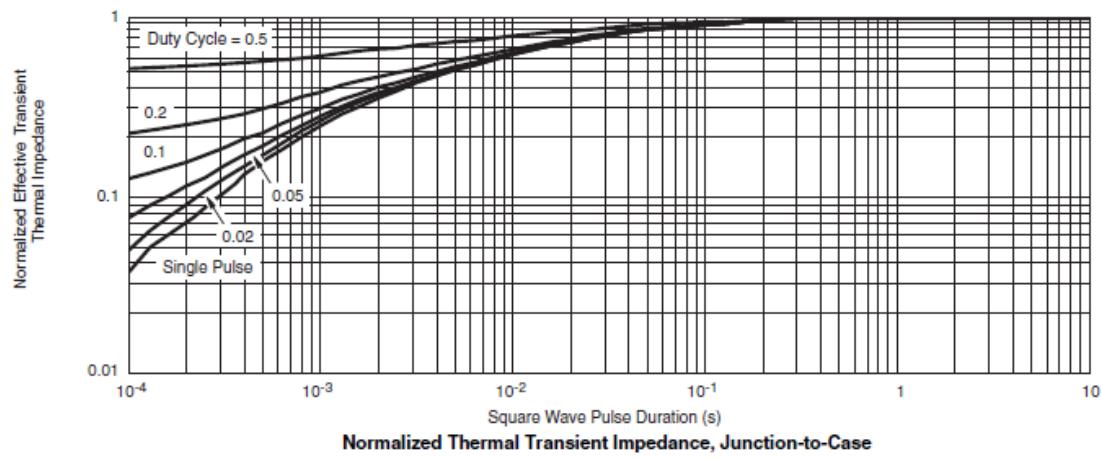
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



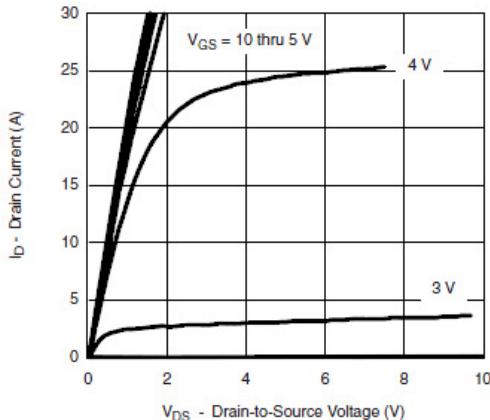
Normalized Thermal Transient Impedance, Junction-to-Case



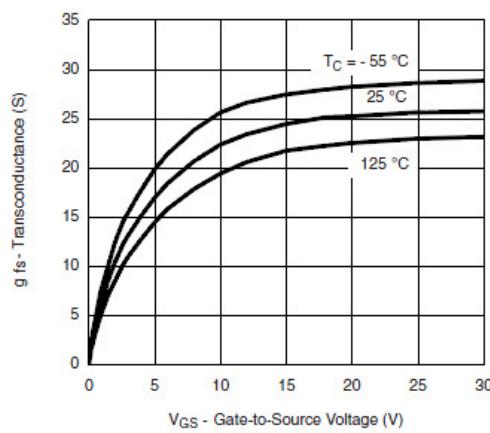
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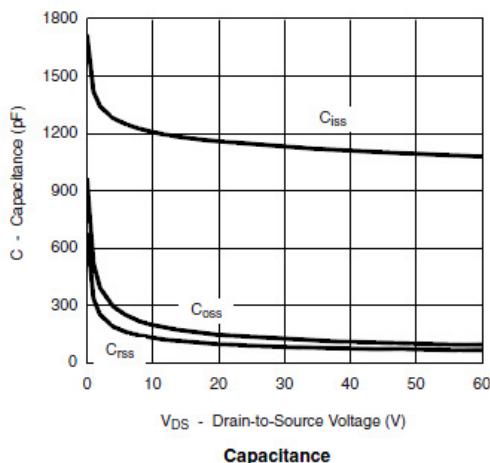
Typical Characteristics (P-Channel)



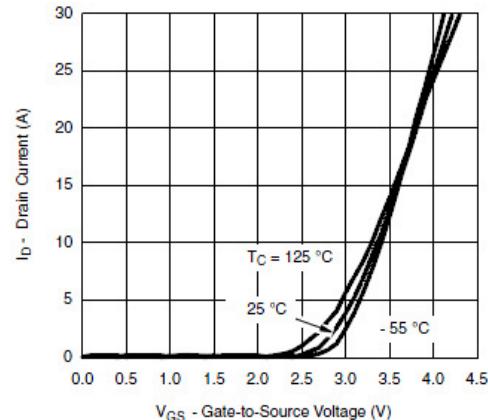
Output Characteristics



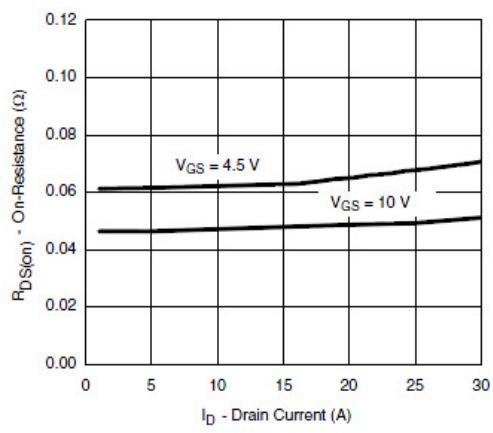
Transconductance



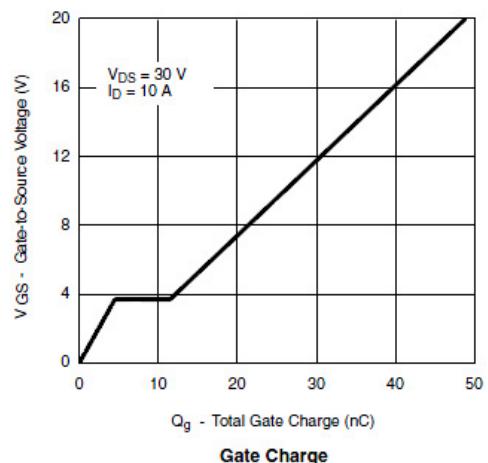
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current



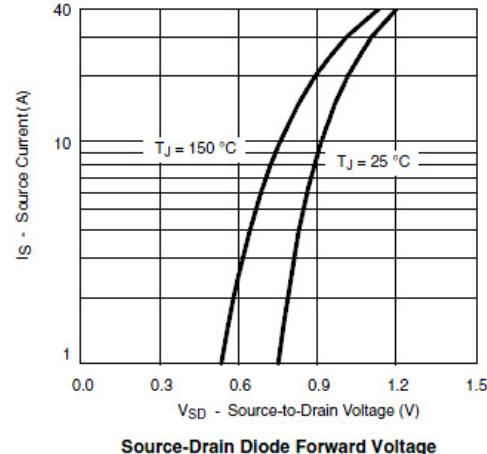
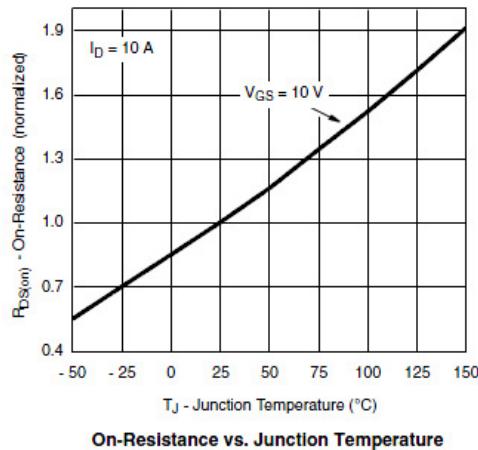
Gate Charge



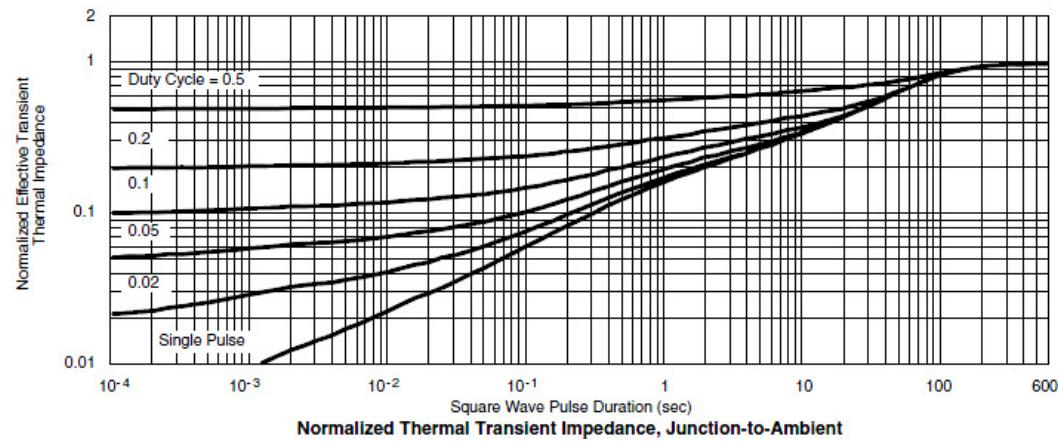
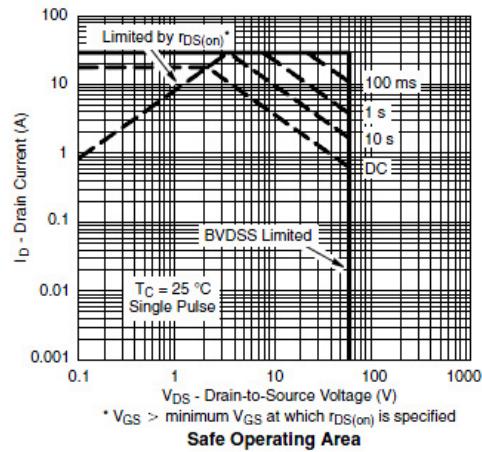
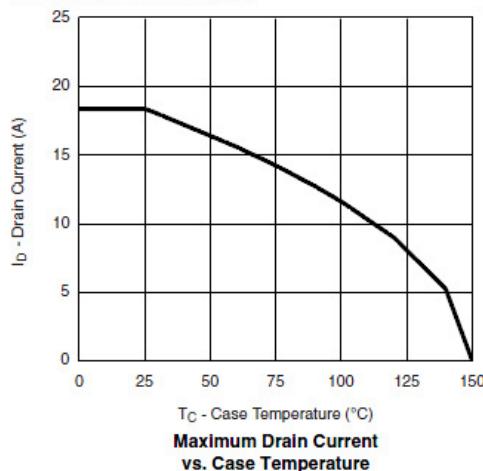
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Typical Characteristics (P-Channel)



THERMAL RATINGS



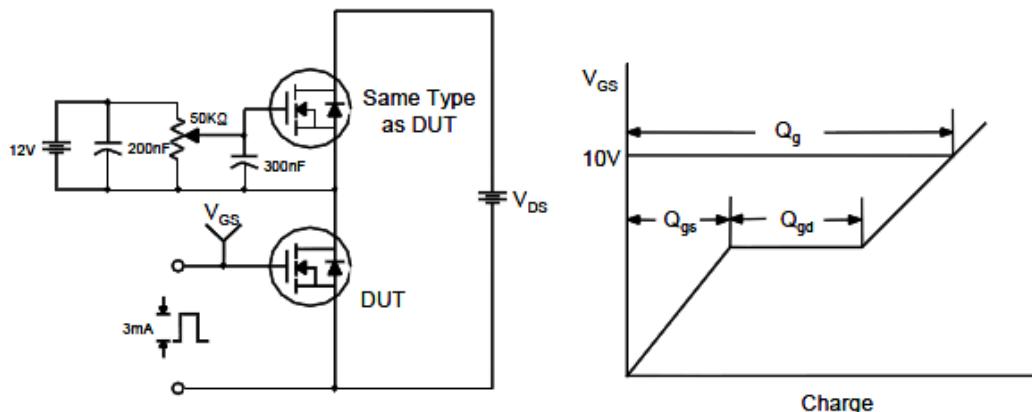


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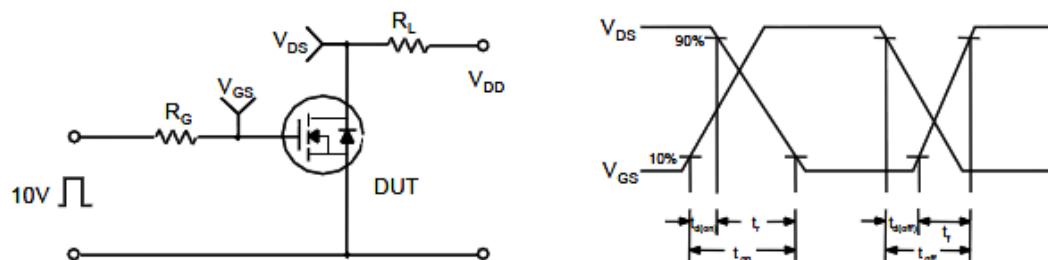
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Typical Characteristics

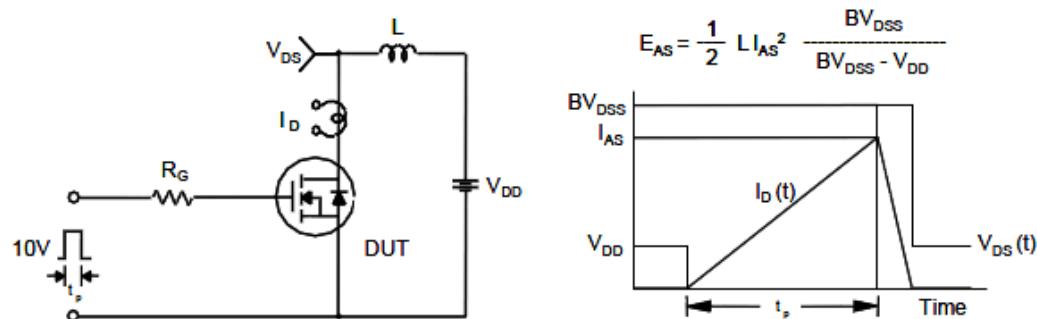
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

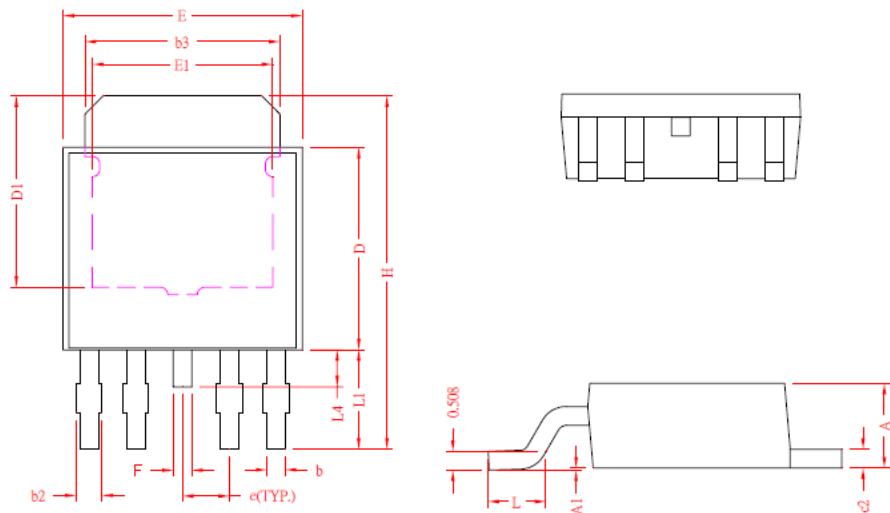


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (TO-252-4L)



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.20	2.40	E	6.40	6.80
A1	0	0.15	E1	3.81	---
b	0.40	0.60	e	1.27 REF.	
b2	0.50	0.80	F	0.40	0.60
b3	5.20	5.50	H	9.40	10.20
c2	0.45	0.55	L	1.40	1.77
D	5.40	5.80	L1	2.40	3.00
D1	4.57	---	L4	0.80	1.20

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