



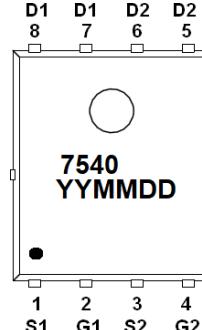
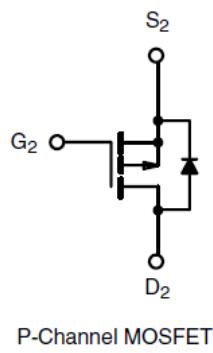
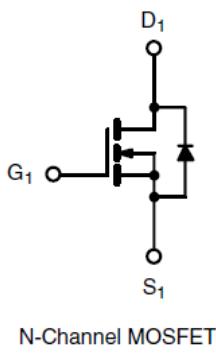
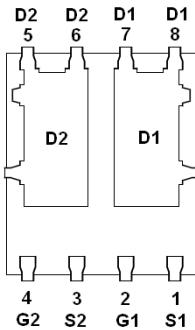
# Alfa-MOS Technology

**AFC7540**  
**20V N & P Pair**  
**Enhancement Mode MOSFET**

## General Description

AFC7540, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent RDS(ON), low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

## Pin Description ( DFN5X6-8L )



## Application

- Point-of-Load Synchronous Rectifier
  - 5 V or 3.3 V BUS Step Down
- Synchronous Buck, Shoot-Thru Resistant

## Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC7540FN568RG	7540	DFN 5X6-8L	Tape & Reel	2500 EA

※ 7540 : Parts Code

※ YYMMDD : Date Code

※ AFC7540FN568RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



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### Absolute Maximum Ratings ( N-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	20	V
Gate -Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	12	A
$T_A=70^\circ\text{C}$		10	
Pulsed Drain Current	$I_{DM}$	20	A
Continuous Source Current(Diode Conduction)	$I_S$	1.5	A
Power Dissipation	$P_D$	3.5	W
$T_A=70^\circ\text{C}$		2.2	
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

### Electrical Characteristics ( N-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.35		1.0	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=20\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS}\geq 5\text{V}, V_{GS}=4.5\text{V}$	30			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5\text{V}, I_D=12\text{A}$		8.4	14	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=10\text{A}$		9.6	16	
		$V_{GS}=1.8\text{V}, I_D=8\text{A}$		12	20	
Forward Transconductance	$g_{FS}$	$V_{DS}=10\text{V}, I_D=7.0\text{A}$		40		S
Diode Forward Voltage	$V_{SD}$	$I_S=9.0\text{A}, V_{GS}=0\text{V}$		0.8	1.3	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}$ $I_D=9.0\text{A}$		13	19	nC
Gate-Source Charge	$Q_{gs}$			2.8		
Gate-Drain Charge	$Q_{gd}$			2.0		
Input Capacitance	$C_{iss}$	$V_{DS}=10\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		1450		pF
Output Capacitance	$C_{oss}$			285		
Reverse Transfer Capacitance	$C_{rss}$			145		
Turn-On Time	$t_{d(on)}$	$V_{DD}=10\text{V}, R_L=1.3\Omega$ $I_D=8.0\text{A}, V_{GEN}=10\text{V}$		10	20	ns
	$t_r$			10	20	
Turn-Off Time	$t_{d(off)}$	$R_G=1\Omega$		25	40	
	$t_f$			10	20	



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### Absolute Maximum Ratings ( P-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	-20	V
Gate -Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	-9.0	A
$T_A=70^\circ\text{C}$		-8.0	
Pulsed Drain Current	$I_{DM}$	-20	A
Continuous Source Current(Diode Conduction)	$I_S$	-1.7	A
Power Dissipation	$P_D$	3.5	W
$T_A=70^\circ\text{C}$		2.2	
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$

### Electrical Characteristics ( P-Channel )

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

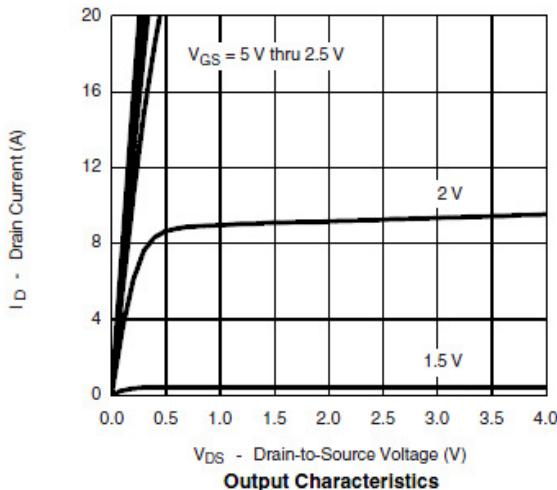
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-0.4		-1.0	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$			-1	uA
		$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			-10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \leq -5\text{V}, V_{GS}=-4.5\text{V}$	-10			A
		$V_{DS} \leq -5\text{V}, V_{GS}=-2.5\text{V}$	-5			
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5\text{V}, I_D=-9\text{A}$		21	33	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}, I_D=-8\text{A}$		32	42	
		$V_{GS}=-1.8\text{V}, I_D=-6\text{A}$		54	68	
Forward Transconductance	$g_{FS}$	$V_{DS}=-9\text{V}, I_D=-6\text{A}$		14		S
Diode Forward Voltage	$V_{SD}$	$I_S=-2.5\text{A}, V_{GS}=0\text{V}$		-0.85	-1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=-15\text{V}, V_{GS}=-4.5\text{V}$ $I_D=-6.0\text{A}$		10	18	nC
Gate-Source Charge	$Q_{gs}$			1.6		
Gate-Drain Charge	$Q_{gd}$			3.0		
Input Capacitance	$C_{iss}$	$V_{DS}=-15\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		950		pF
Output Capacitance	$C_{oss}$			200		
Reverse Transfer Capacitance	$C_{rss}$			175		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-15\text{V}, R_L=15\Omega$ $I_D=-5.0\text{A}, V_{GEN}=-10\text{V}$		8	18	ns
	$t_r$			8	18	
Turn-Off Time	$t_{d(off)}$	$R_G=6\Omega$		25	50	
	$t_f$			25	35	



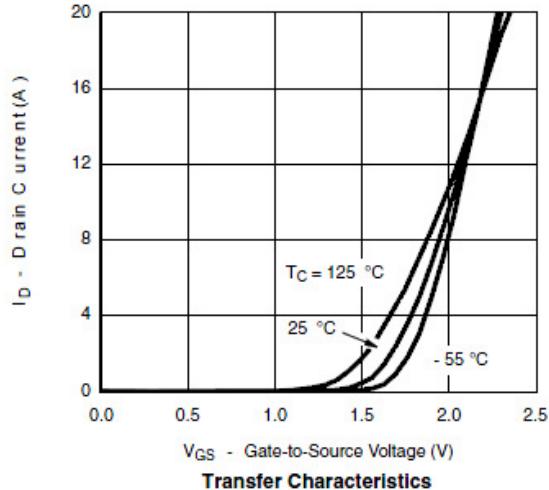
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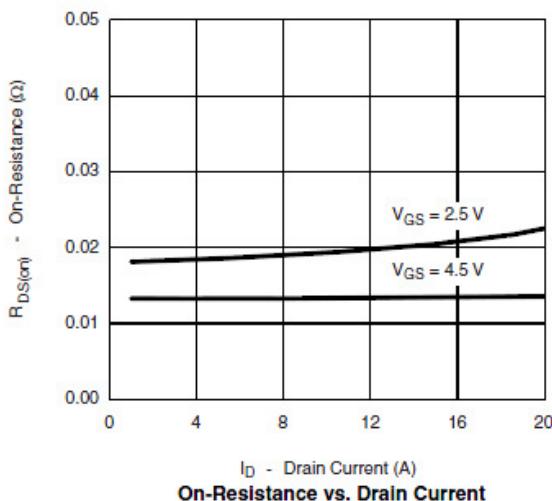
### Typical Characteristics ( N-Channel )



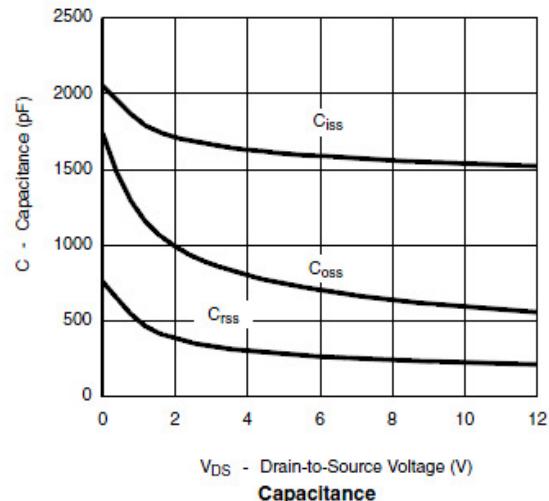
Output Characteristics



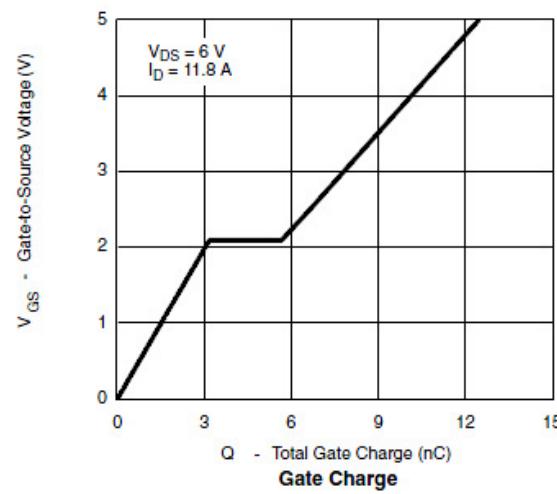
Transfer Characteristics



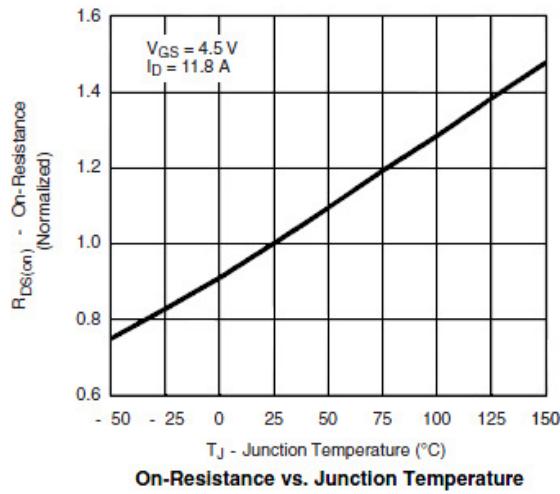
On-Resistance vs. Drain Current



Capacitance



Gate Charge



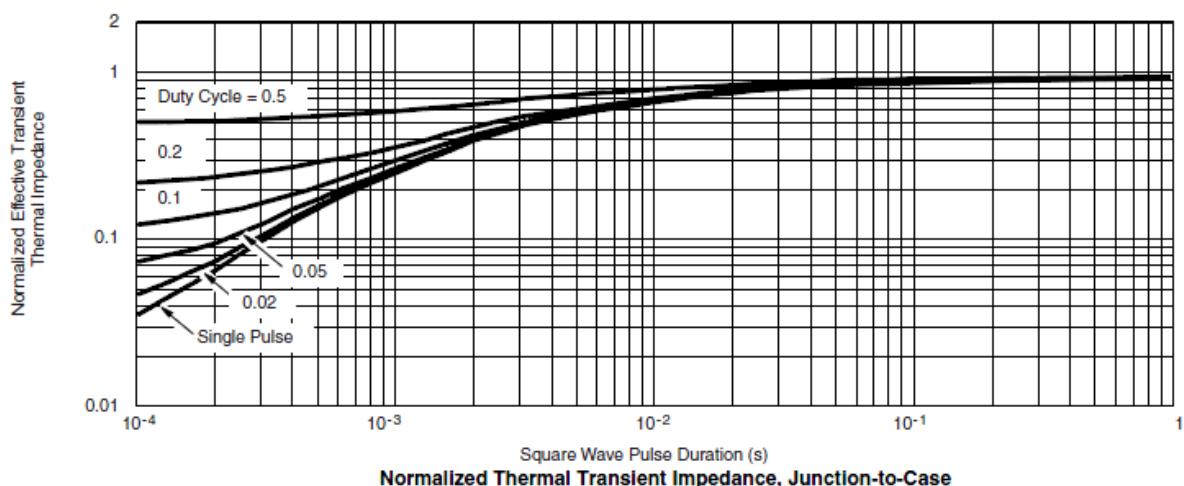
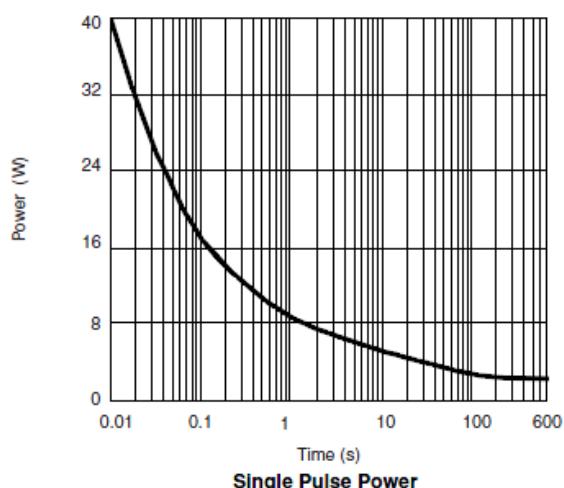
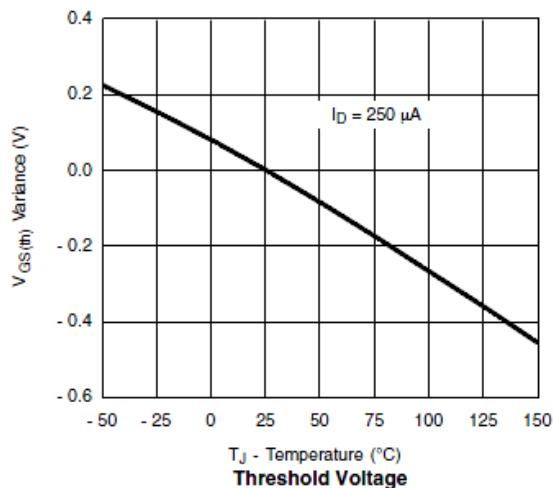
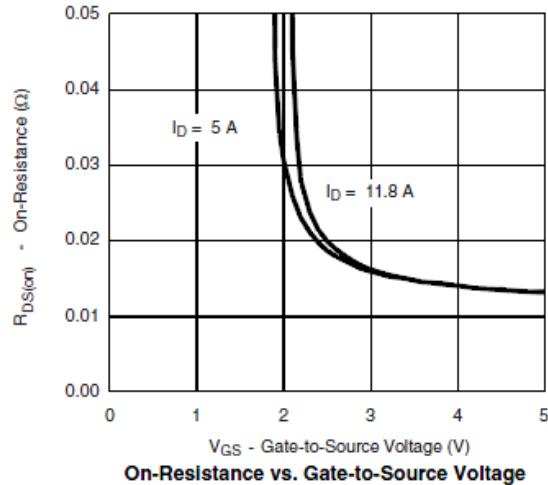
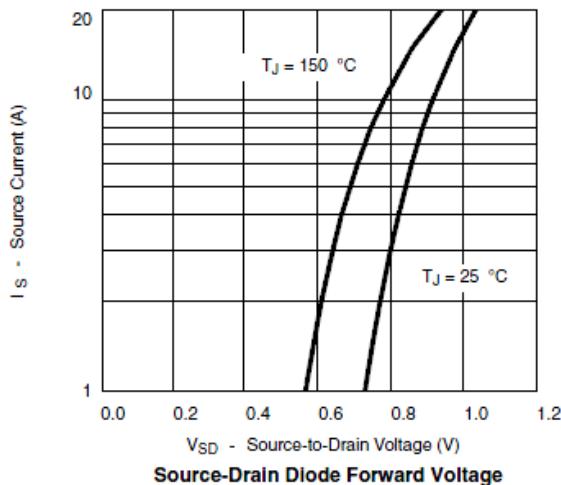
On-Resistance vs. Junction Temperature



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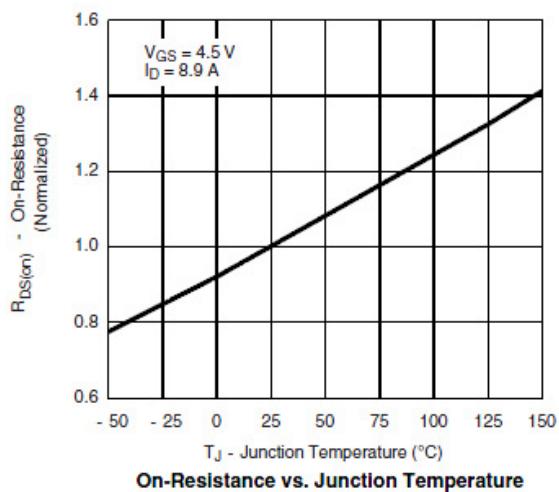
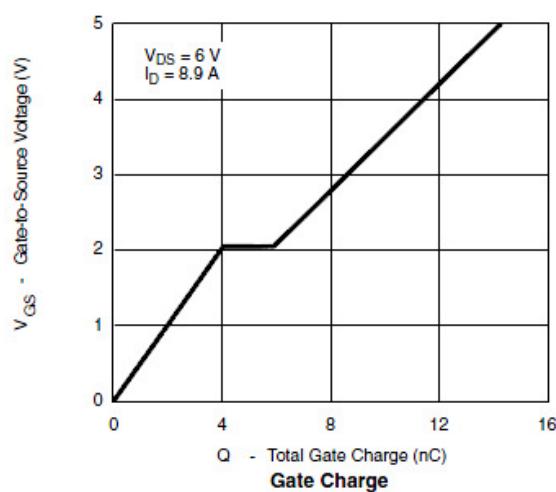
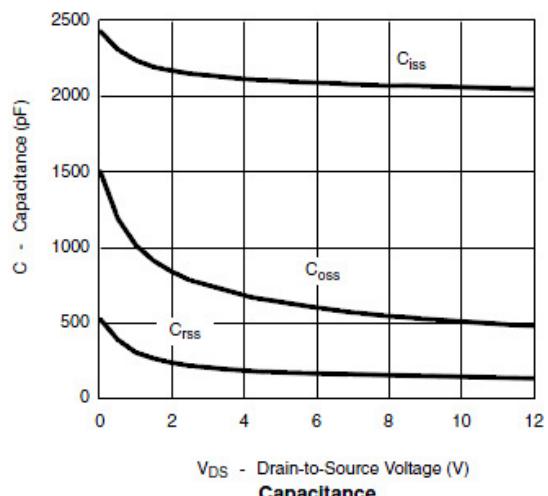
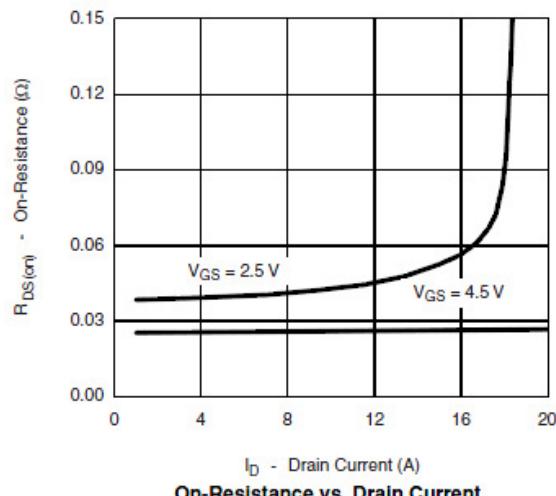
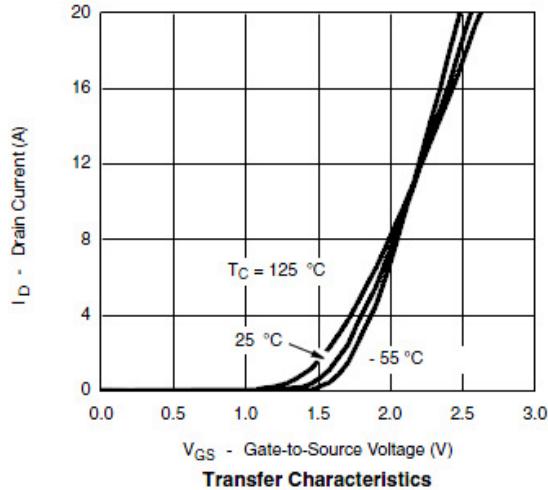
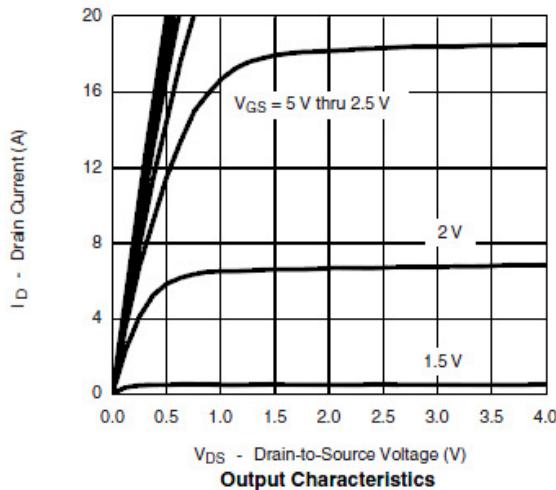
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### Typical Characteristics ( N-Channel )





**Typical Characteristics ( P-Channel )**

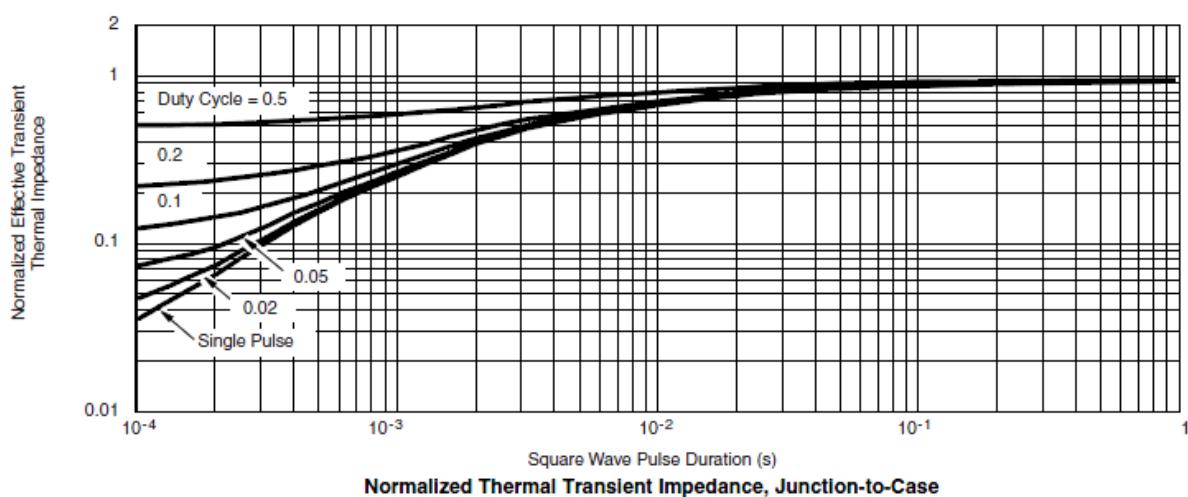
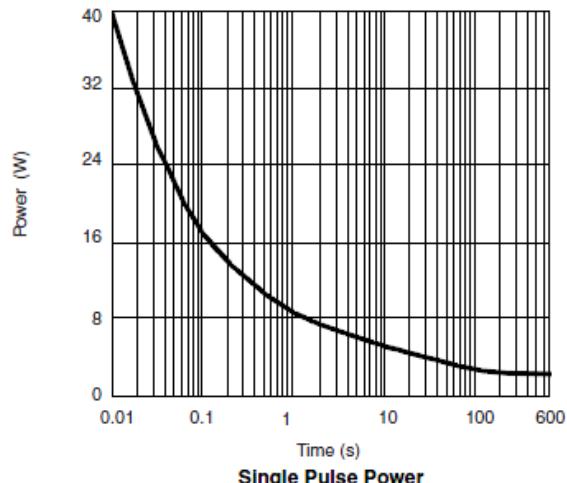
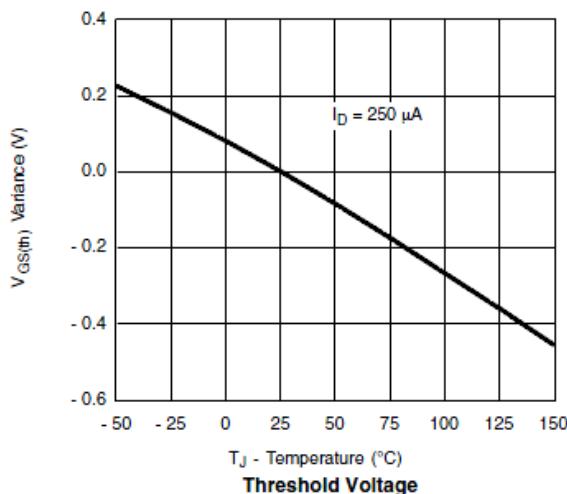
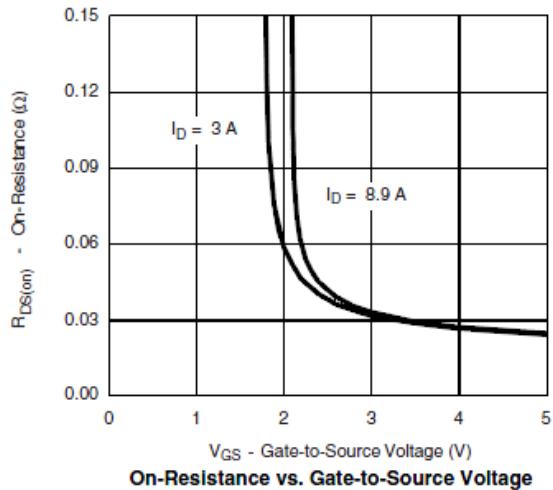
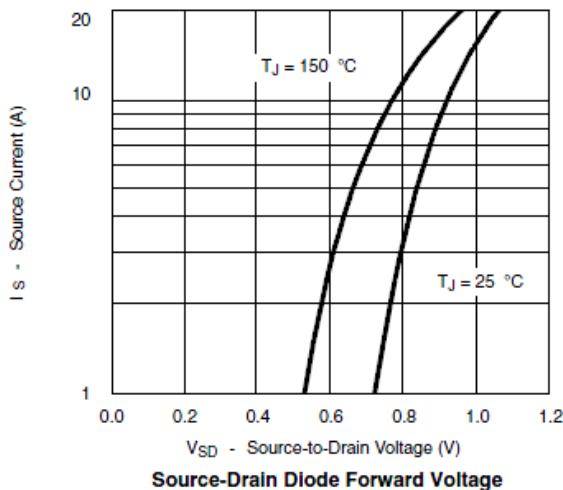




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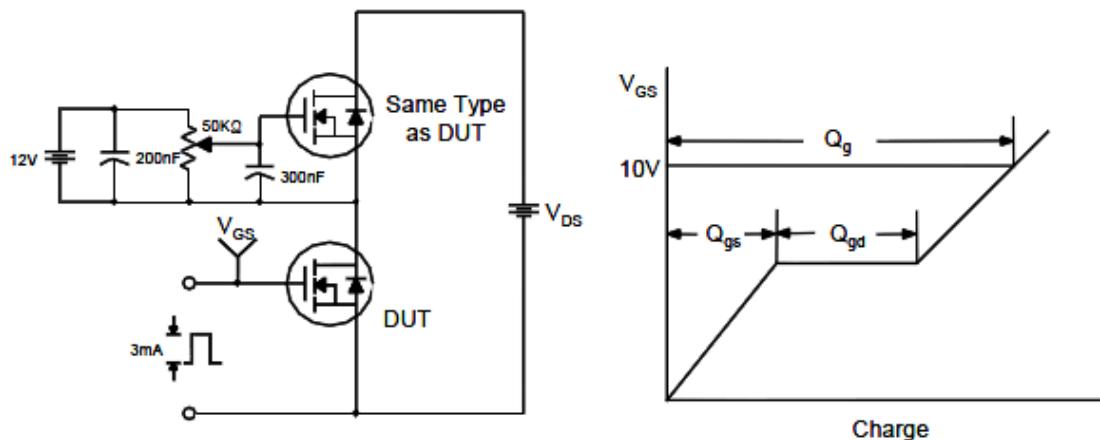
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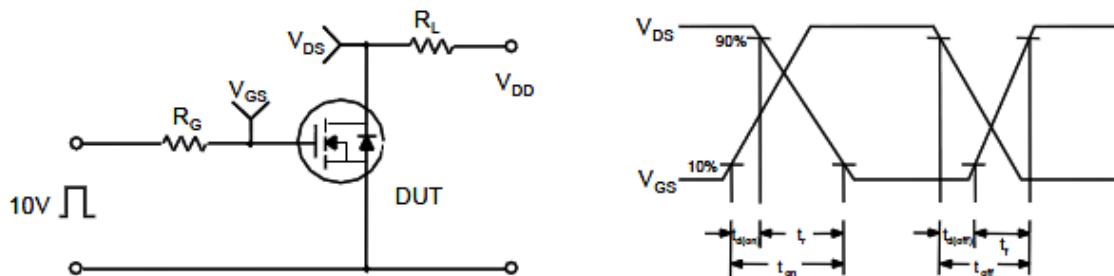


## Typical Characteristics

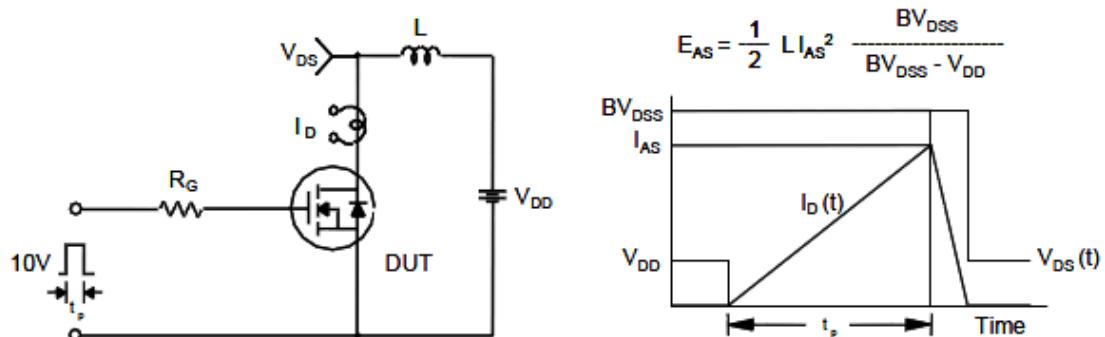
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

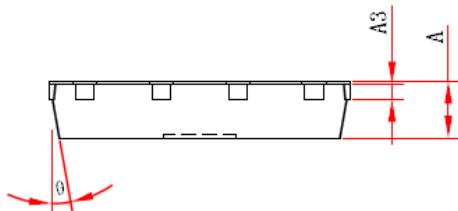
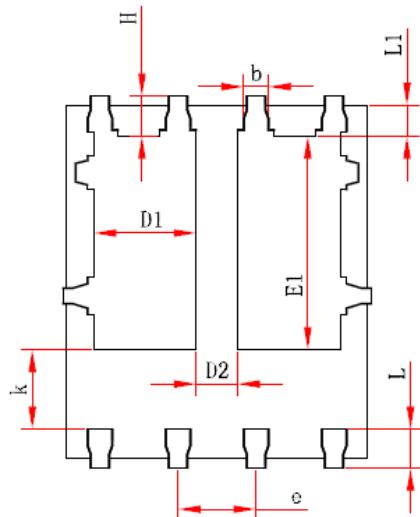
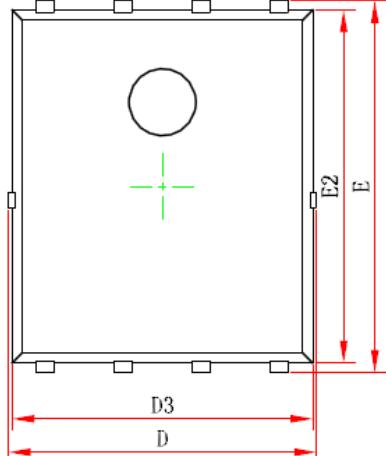




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**Package Information ( DFN 5X6-8L )**



Top View

Bottom View

Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254 REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.375	3.575	0.133	0.141
D3	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

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