



**Alfa-MOS  
Technology**

**AFN1330KS**  
**60V N-Channel**  
**Enhancement Mode MOSFET**

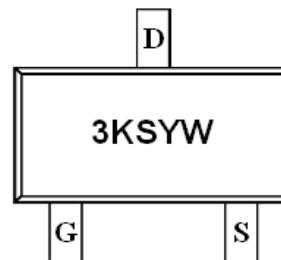
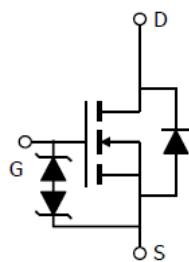
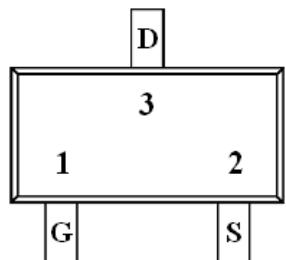
## General Description

AFN1330KS, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

- 60V/0.5A ,  $R_{DS(ON)}=2400m\Omega$ @ $V_{GS}=10V$
- 60V/0.3A ,  $R_{DS(ON)}=3000m\Omega$ @ $V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- ESD Protection ( 2KV ) Diode design-in
- SOT-323 package design

## Pin Description ( SOT-323 )



## Application

- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- High saturation current capability. Direct Logic-Level Interface: TTL/CMOS
- Battery Operated Systems
- Solid-State Relays

## Pin Define

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN1330KSS32RG	3KSYW	SOT-323	Tape & Reel	3000 EA

※ 3KS Parts code

※ Y Year code ( 0 ~ 9 )

※ W Week code ( A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52 )

※ AFN1330KSS32RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



### Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate –Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	0.5	A
		0.3	
Pulsed Drain Current	$I_{DM}$	1.0	A
Continuous Source Current(Diode Conduction)	$I_S$	0.4	A
Power Dissipation	$P_D$	0.35	W
		0.22	
Operating Junction Temperature	$T_J$	150	150
Storage Temperature Range	$T_{STG}$	-55/150	-55/150
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	120

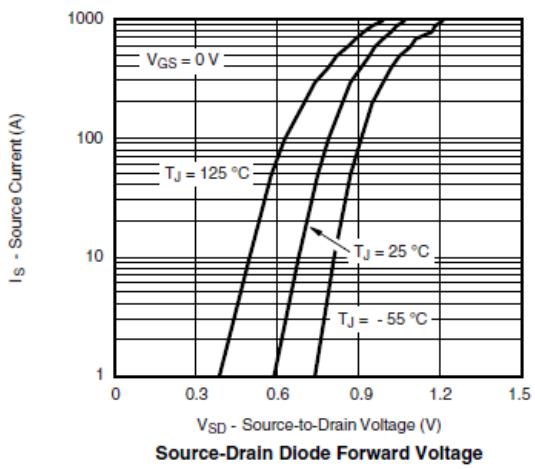
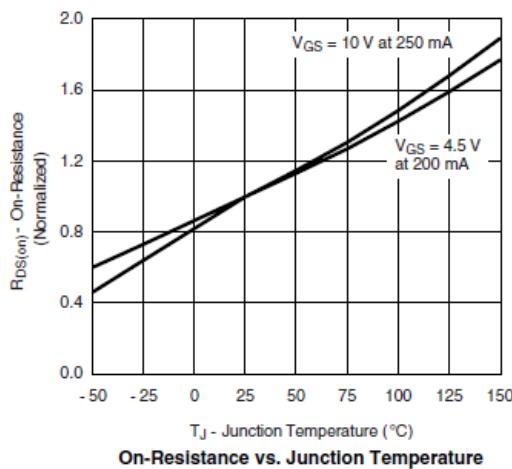
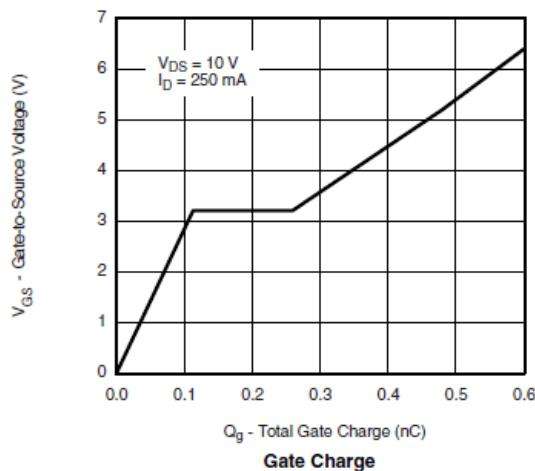
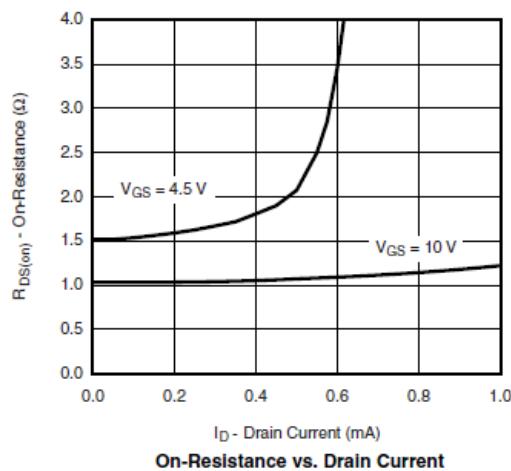
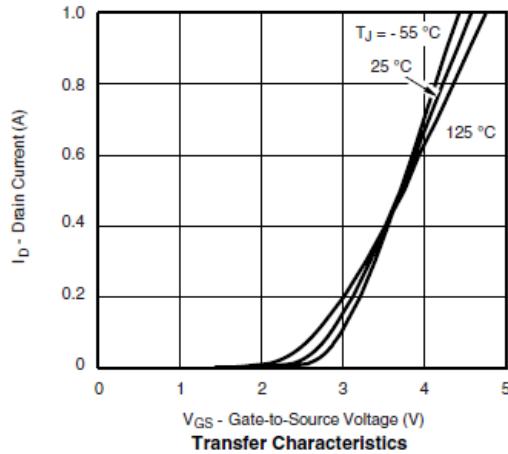
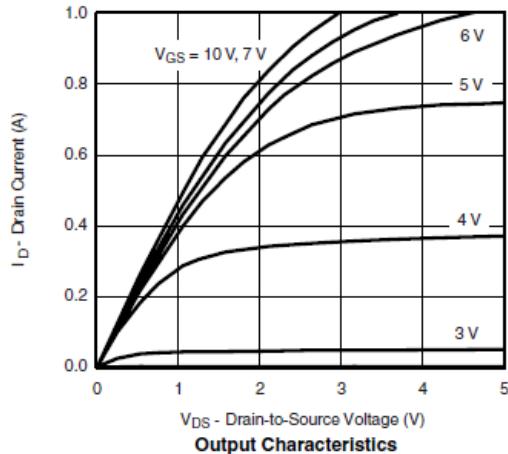
### Electrical Characteristics

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		2.0	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			3	$\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$			1	$\mu\text{A}$
		$V_{DS}=60\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			10	
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=0.5\text{A}$	1200	2400		$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=0.3\text{A}$	1600	3000		
Forward Transconductance	$g_{FS}$	$V_{DS}=10\text{V}, I_D=0.2\text{A}$		0.2		S
Diode Forward Voltage	$V_{SD}$	$I_S=0.2\text{A}, V_{GS}=0\text{V}$		0.75	1.4	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}$ $I_D=0.25\text{A}$		450		$\text{pC}$
Gate-Source Charge	$Q_{gs}$			110		
Gate-Drain Charge	$Q_{gd}$			150		
Input Capacitance	$C_{iss}$	$V_{DS}=25\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		30		$\text{pF}$
Output Capacitance	$C_{oss}$			8		
Reverse Transfer Capacitance	$C_{rss}$			5		
Turn-On Time	$t_{d(on)}$	$V_{DD}=30\text{V}, R_L=150\Omega$ $I_D=0.2\text{A}, V_{GEN}=10\text{V}$ $R_G=10\Omega$		4	10	$\text{ns}$
	$t_r$			5	15	
Turn-Off Time	$t_{d(off)}$			12	20	
	$t_f$			10	20	

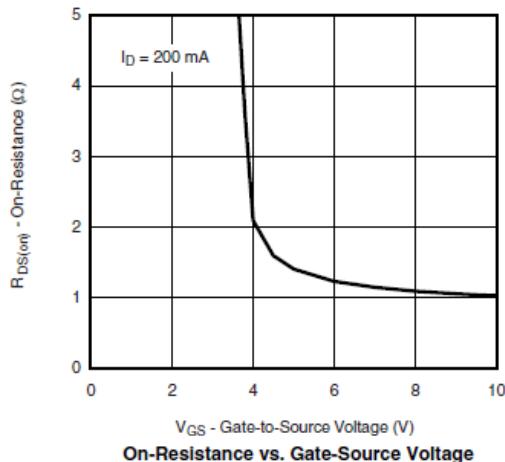


### Typical Characteristics

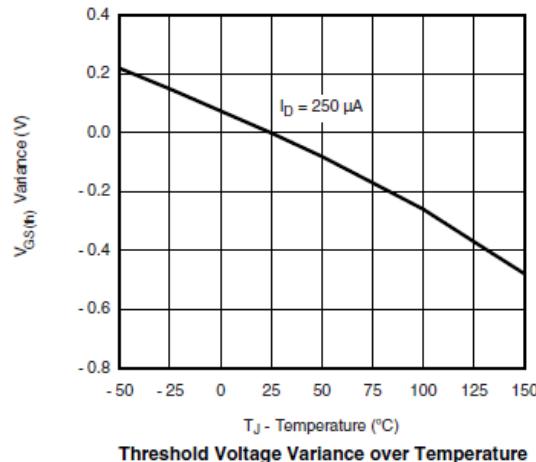




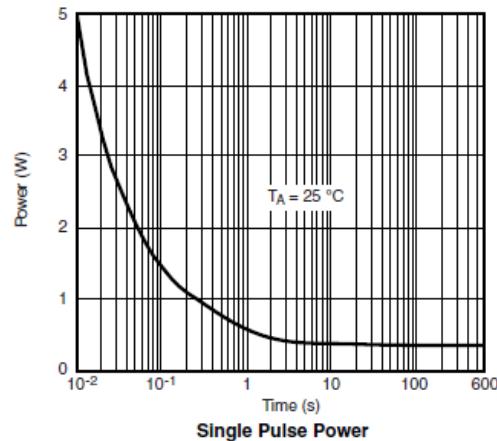
### Typical Characteristics



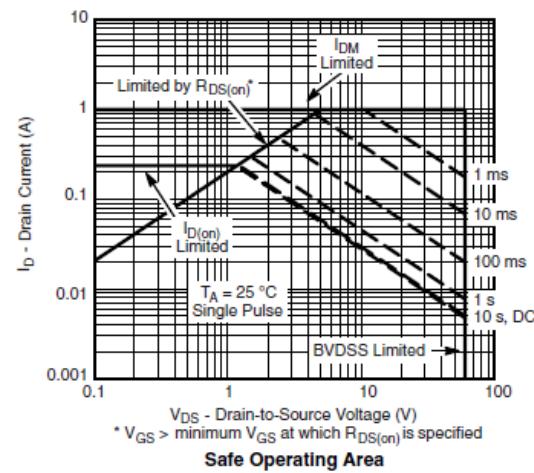
On-Resistance vs. Gate-Source Voltage



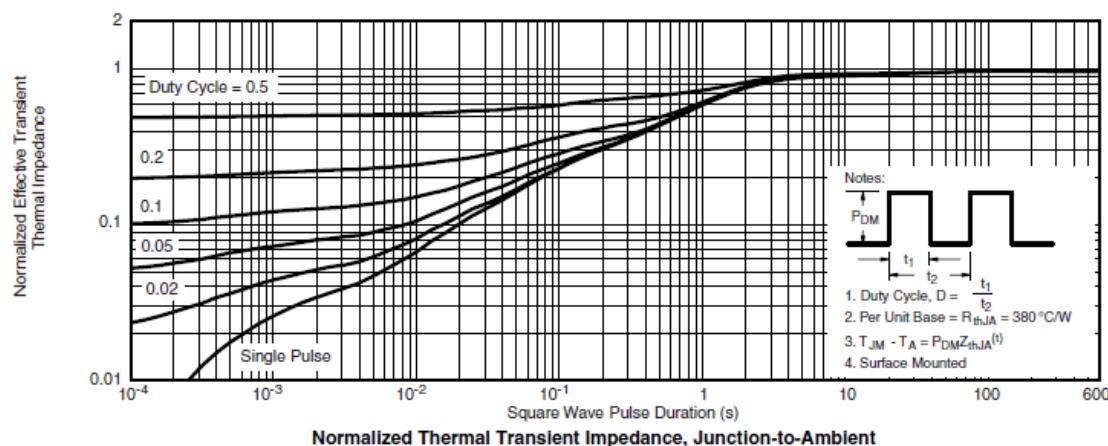
Threshold Voltage Variance over Temperature



Single Pulse Power



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

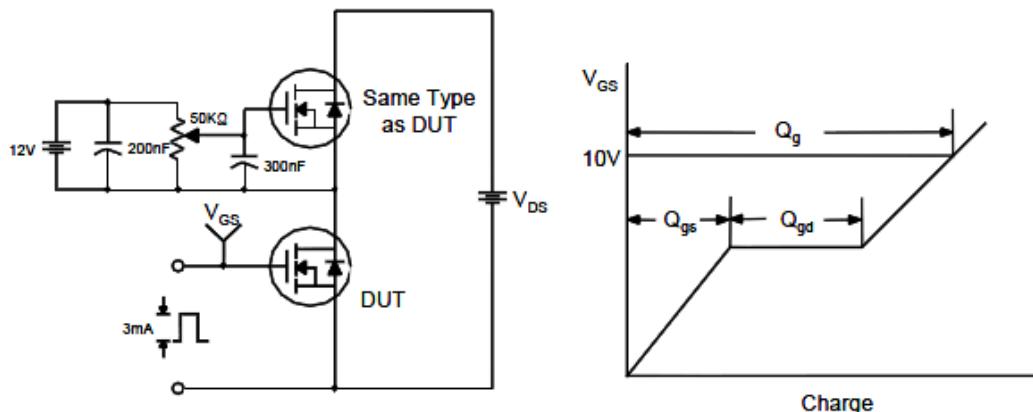


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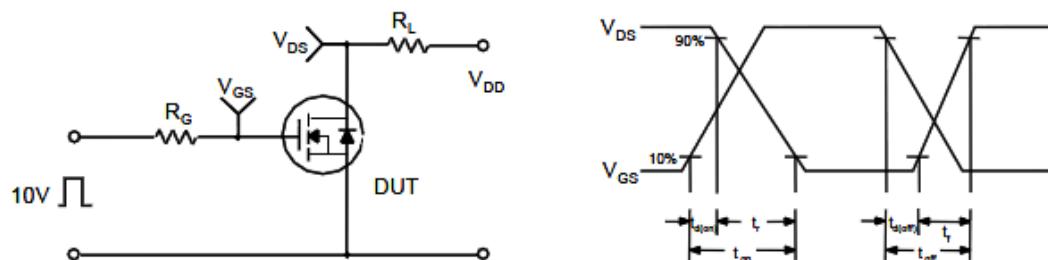
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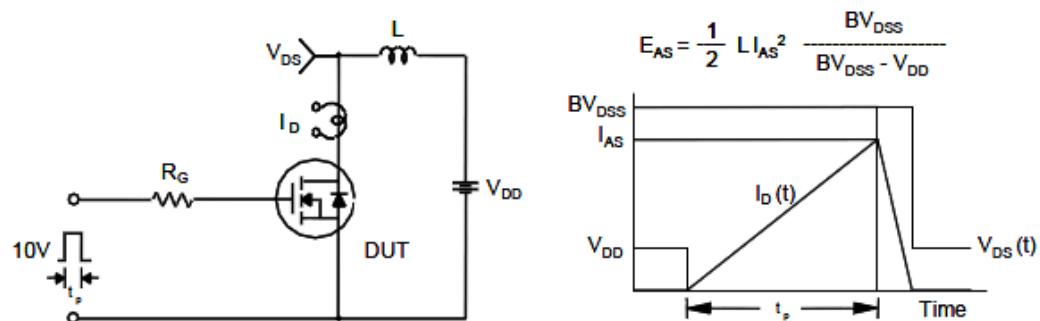
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

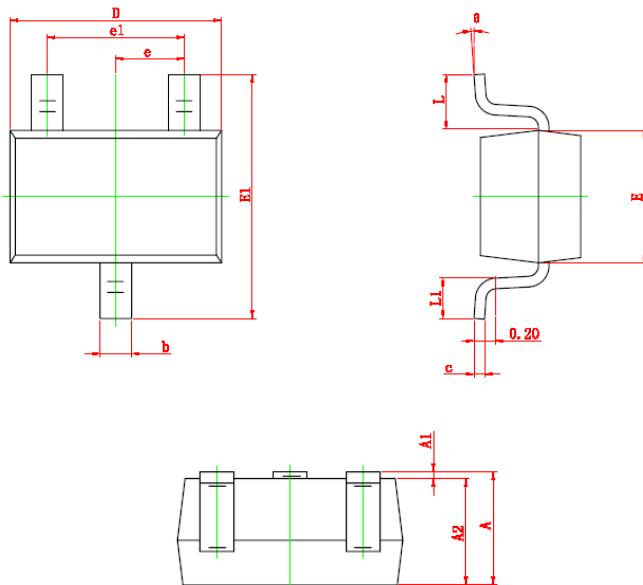


Unclamped Inductive Switching Test Circuit & Waveforms





**Package Information ( SOT-323 )**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

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