



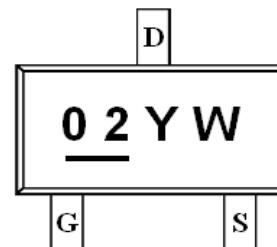
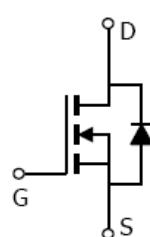
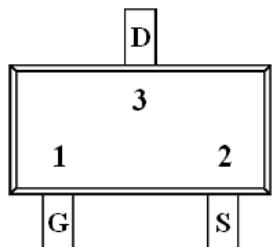
**Alfa-MOS  
Technology**

**AFN3402A  
30V N-Channel  
Enhancement Mode MOSFET**

## General Description

AFN3402A, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

## Pin Description ( SOT-23 )



## Application

- Portable Equipment
- Battery Powered System
- Net Working System

## Pin Define

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN3402AS23RG	02YW	SOT-23	Tape & Reel	3000 EA

※ 02 parts code

※ Y year code ( 0 ~ 9 )

※ W week code ( A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52 )

※ AFN3402AS23RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



### Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	30	V
Gate –Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	3.6	A
$T_A=70^\circ\text{C}$		2.0	
Pulsed Drain Current	$I_{DM}$	10	A
Continuous Source Current(Diode Conduction)	$I_S$	1.6	A
Power Dissipation	$P_D$	1.25	W
$T_A=70^\circ\text{C}$		0.8	
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ\text{C}/\text{W}$

### Electrical Characteristics

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

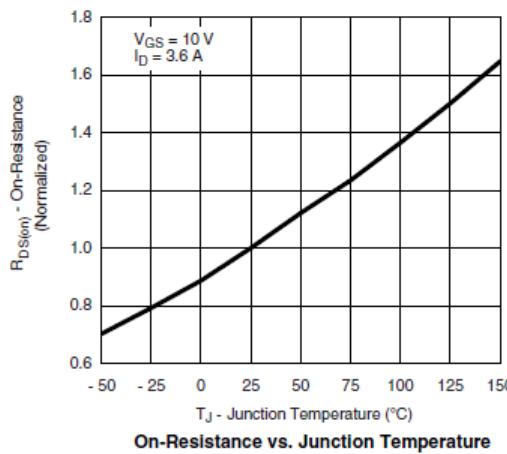
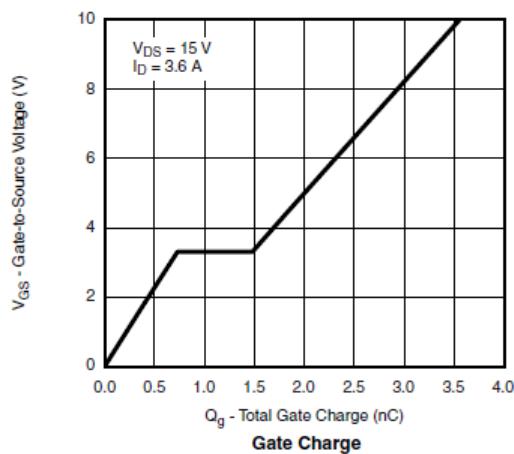
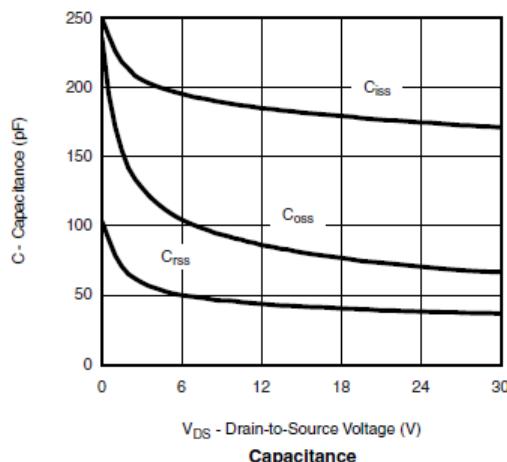
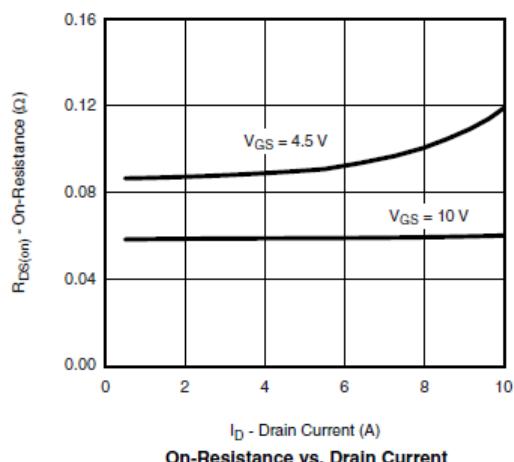
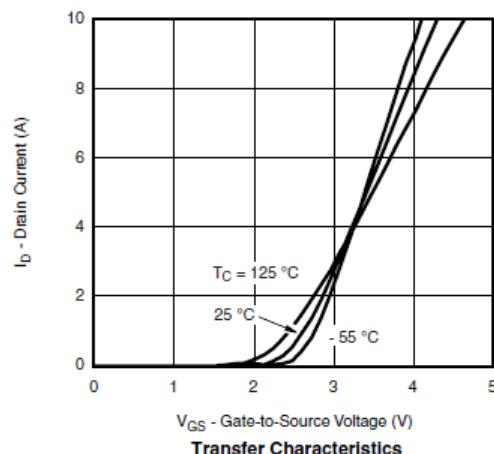
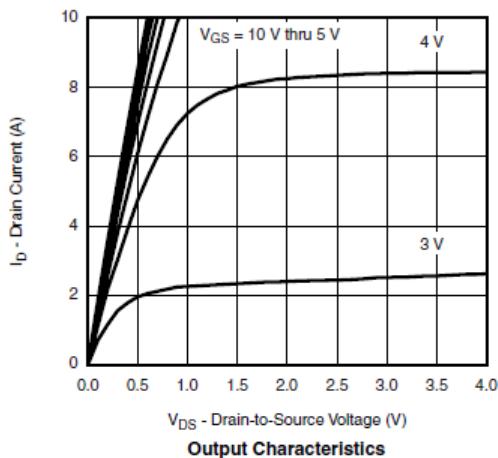
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.3		1.2	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$			1	
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}$				
		$T_J=85^\circ\text{C}$			30	uA
On-State Drain Current	$I_{D(on)}$	$V_{DS}\geq 5\text{V}, V_{GS}=4.5\text{V}$	30			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=2.4\text{A}$		72	82	
		$V_{GS}=4.5\text{V}, I_D=2.0\text{A}$		77	87	mΩ
		$V_{GS}=2.5\text{V}, I_D=1.5\text{A}$		100	110	
Forward Transconductance	$g_{FS}$	$V_{DS}=10\text{V}, I_D=1.6\text{A}$		20		S
Diode Forward Voltage	$V_{SD}$	$I_S=1.7\text{A}, V_{GS}=0\text{V}$		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}$ $I_D=3.6\text{A}$		2.3	3	nC
Gate-Source Charge	$Q_{gs}$			1.0		
Gate-Drain Charge	$Q_{gd}$			0.6		
Input Capacitance	$C_{iss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		280		pF
Output Capacitance	$C_{oss}$			40		
Reverse Transfer Capacitance	$C_{rss}$			20		
Turn-On Time	$t_{d(on)}$	$V_{DD}=15\text{V}, R_L=15\Omega$ $I_D=1.0\text{A}, V_{GEN}=10\text{V}$		10	15	ns
	$t_r$			12	20	
Turn-Off Time	$t_{d(off)}$			15	25	
	$t_f$			10	15	



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### Typical Characteristics

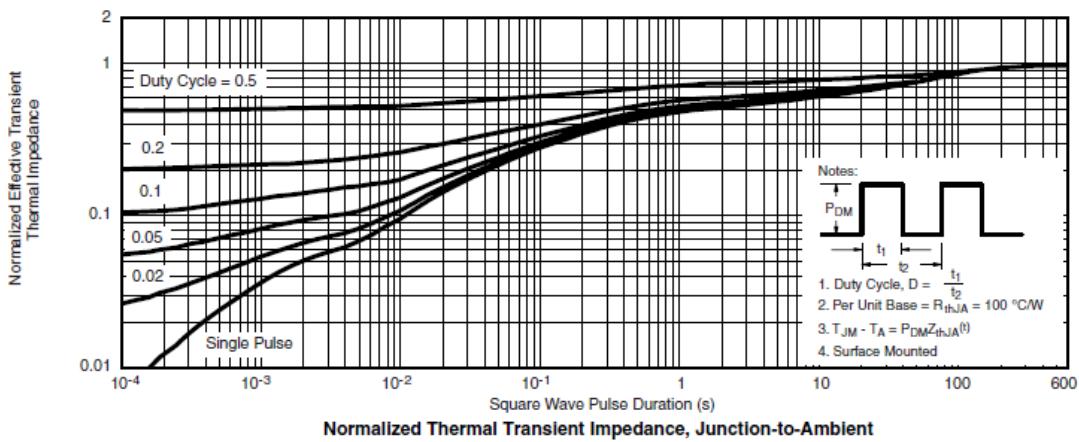
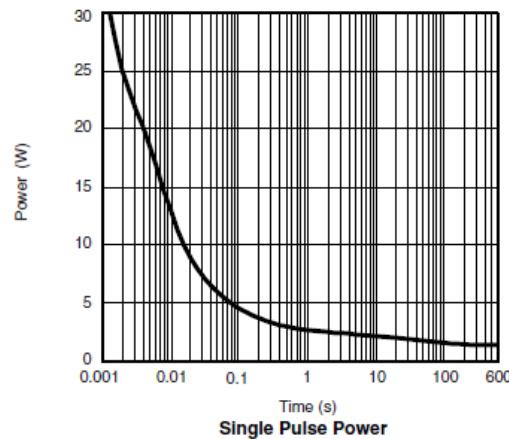
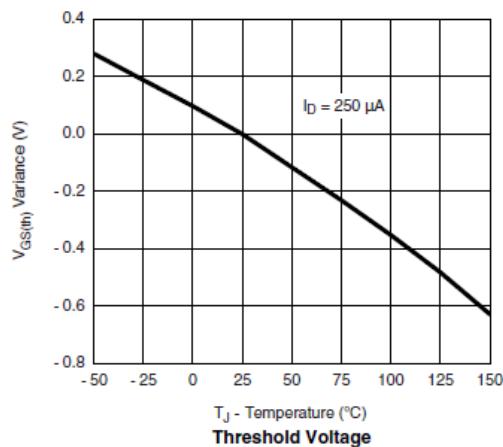
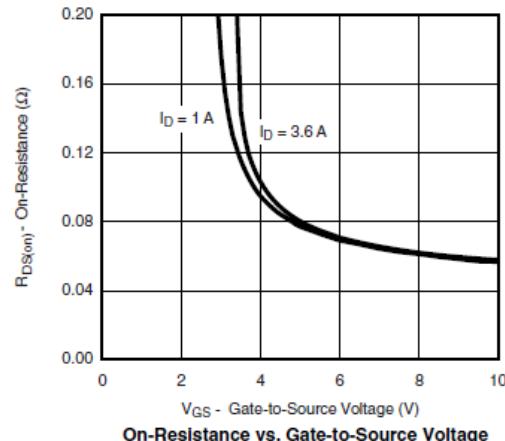
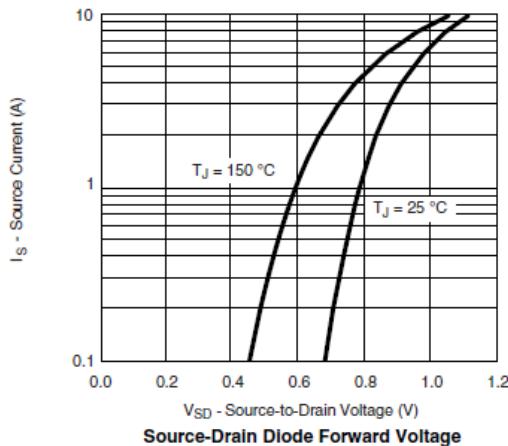




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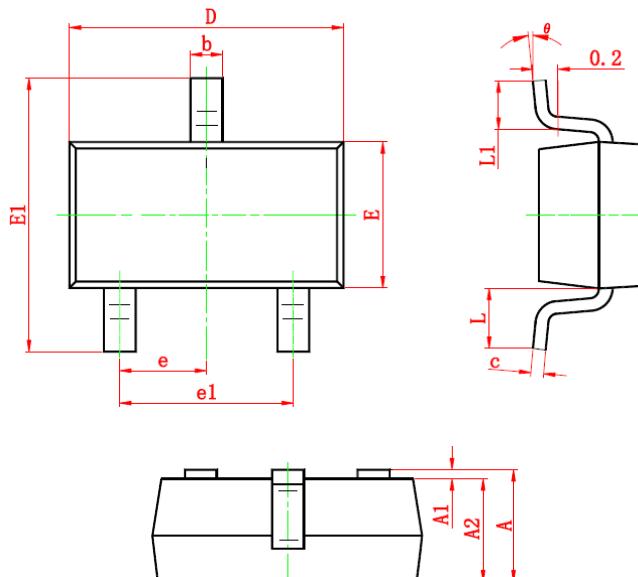
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### Typical Characteristics





**Package Information ( SOT-23 )**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	6°

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2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)  
Tel : 886 2) 2651 3928  
Fax : 886 2) 2786 8483  
<http://www.alfa-mos.com>