



### General Description

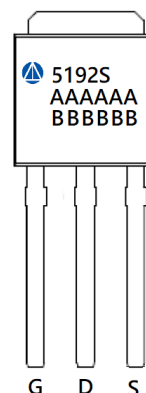
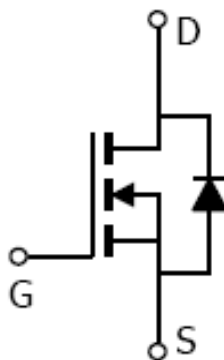
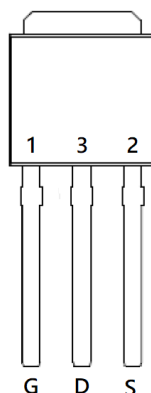
AFN5192S, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

### Features

- 60V/20A,  $R_{DS(ON)}=10m\Omega@V_{GS}=10V$
- 60V/10A,  $R_{DS(ON)}=14m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- TO-251-3L package design

### Pin Description ( TO-251-3L )



### Application

- Primary Side Switch
- POL Synchronous buck converter
- LED Backlight for LCD TV

### Pin Define

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

### Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN5192ST251TG	5192S	TO-251-3L	Tube	80 EA

- ※ A Lot code
- ※ B Date code
- ※ AFN5192ST251TG : Tube ; Pb- Free ; Halogen –Free



### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

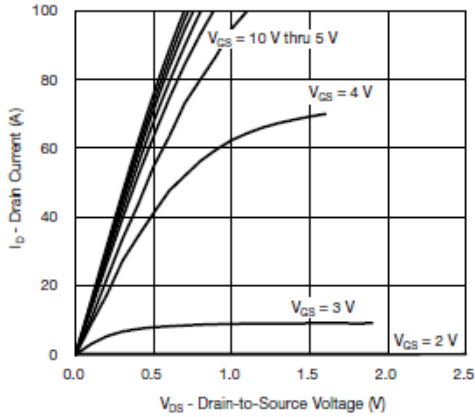
Parameter	Symbol	Typical	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current ( $T_J=150^\circ\text{C}$ )	$I_{DSM}$	$T_C=25^\circ\text{C}$   $T_C=70^\circ\text{C}$	A
		$T_A=25^\circ\text{C}$   $T_A=70^\circ\text{C}$	
Pulsed Drain Current ( $t=100\mu\text{s}$ )	$I_{DM}$	100	
Continuous Source Current (Diode Conduction)	$I_S$	$T_C=25^\circ\text{C}$	
		$T_A=25^\circ\text{C}$	4.5
Single Pulse Avalanche Current	$I_{AS}$ $E_{AS}$	$L=0.1\text{mH}$	20
			20
Power Dissipation	$P_D$	$T_C=25^\circ\text{C}$   $T_C=75^\circ\text{C}$	31   20
		$T_A=25^\circ\text{C}$   $T_A=75^\circ\text{C}$	5.0   3.2
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	23	$^\circ\text{C/W}$
Maximum Junction-to-Case (Drain)	$R_{\theta JA}$	1.5	

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ Unless otherwise noted)

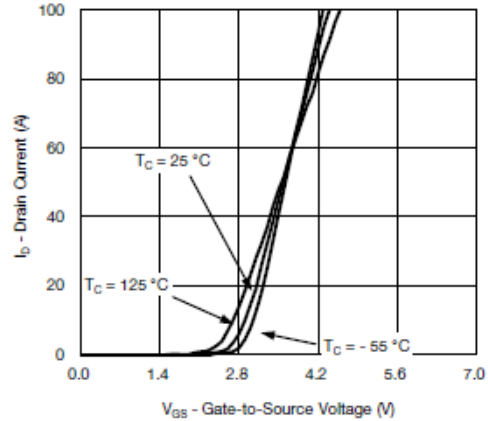
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		2.5	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=48V, V_{GS}=0V$			1	uA
		$V_{DS}=48V, V_{GS}=0V$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5V, V_{GS}=10V$	30			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		8.5	10	m $\Omega$
		$V_{GS}=4.5V, I_D=10A$		11.5	14	
Forward Transconductance	$g_{FS}$	$V_{DS}=15V, I_D=10A$		80		S
Diode Forward Voltage	$V_{SD}$	$I_S=5A, V_{GS}=0V$		0.78	1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=30V, V_{GS}=4.5V$ $I_D \equiv 10A$		9.2	15	nC
Gate-Source Charge	$Q_{gs}$			4.2		
Gate-Drain Charge	$Q_{gd}$			2.5		
Gate Resistance	$R_g$	$f=1\text{MHz}$	0.4	2.3	4.0	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=30V, V_{GS}=0V$ $f=1\text{MHz}$		1450		pF
Output Capacitance	$C_{oss}$			1525		
Reverse Transfer Capacitance	$C_{rss}$			45		
Turn-On Time	$t_{d(on)}$	$V_{DD}=30V, R_L=3.0\Omega$ $I_D \equiv 10A, V_{GEN}=10V$ $R_G=1\Omega$		10	20	ns
	$t_r$			10	20	
Turn-Off Time	$t_{d(off)}$			25	50	
	$t_f$			10	20	



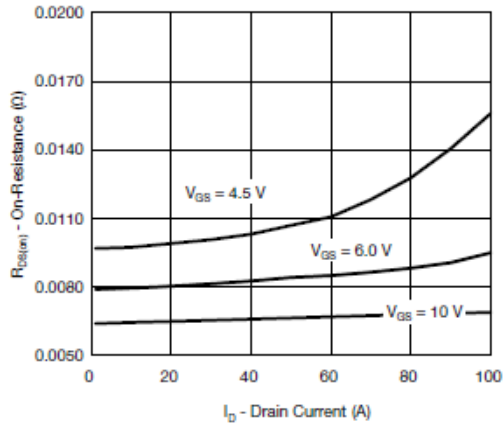
## Typical Characteristics



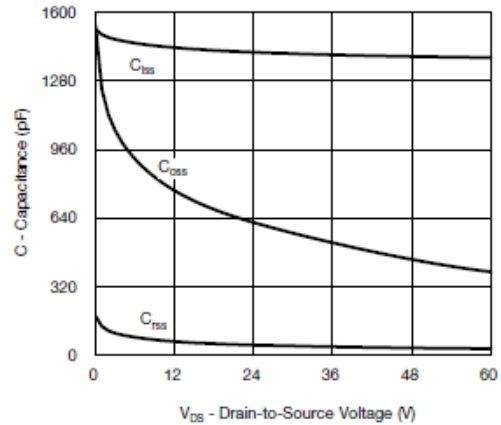
Output Characteristics



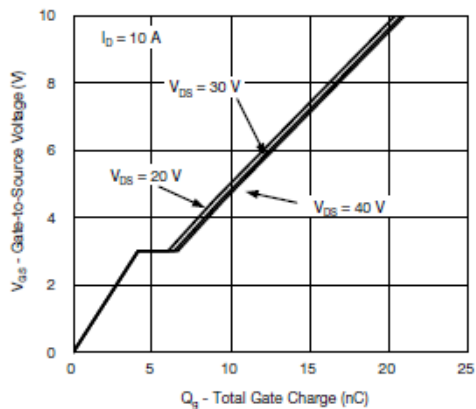
Transfer Characteristics



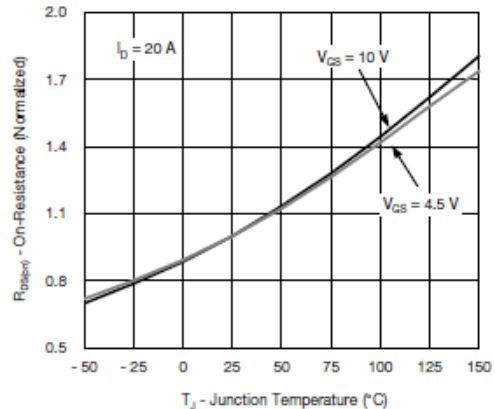
On-Resistance vs. Drain Current



Capacitance



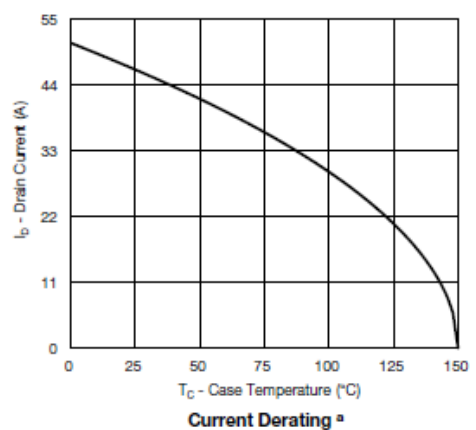
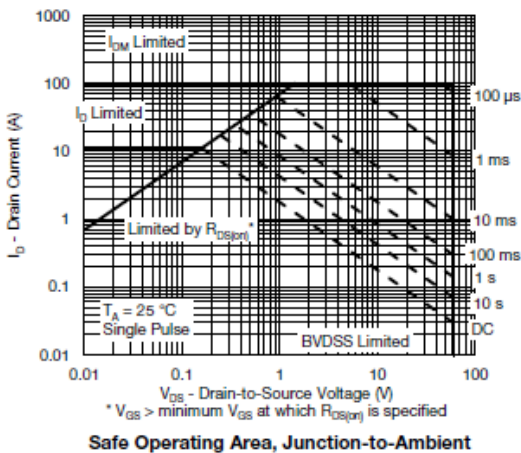
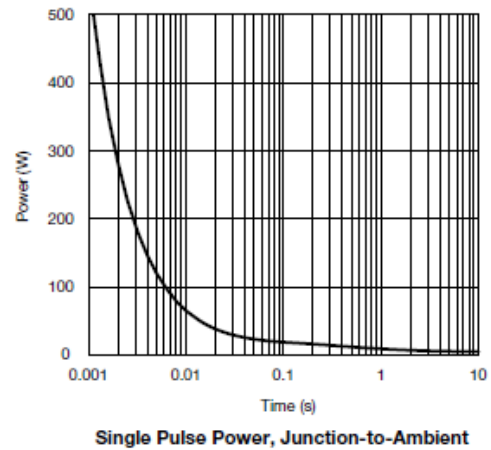
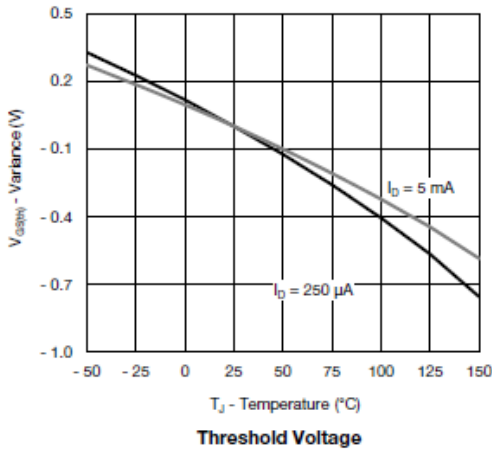
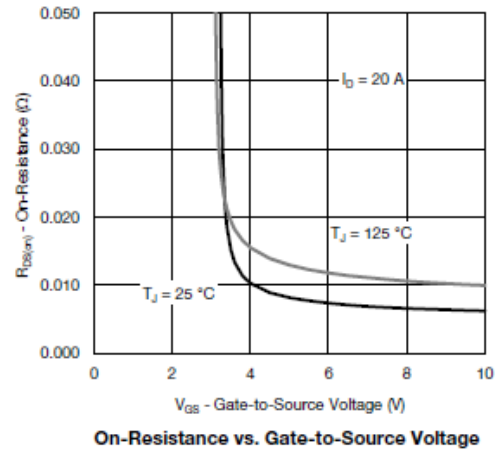
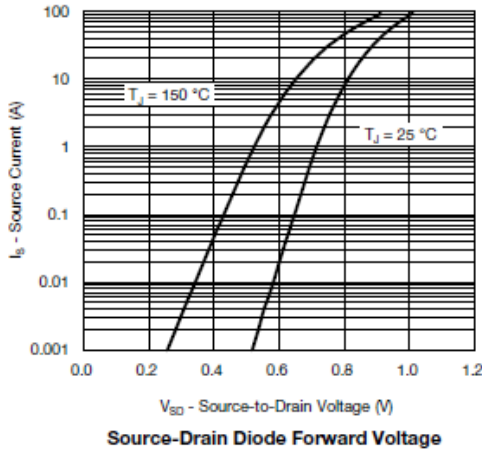
Gate Charge



On-Resistance vs. Junction Temperature

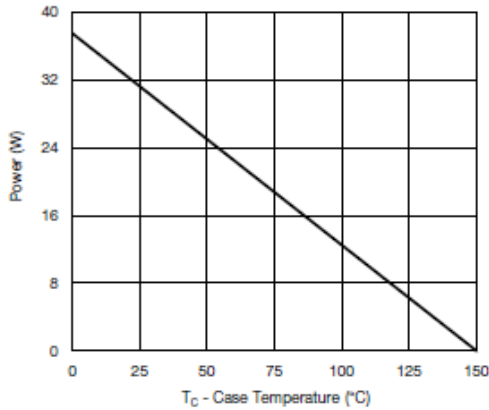


## Typical Characteristics

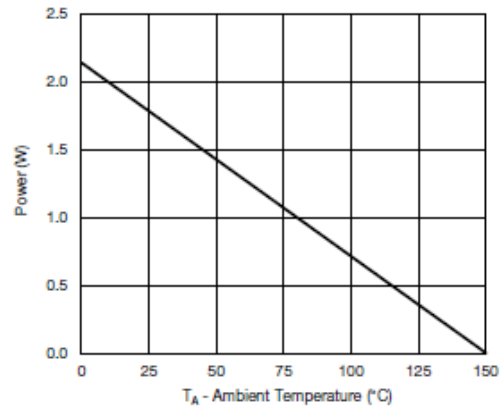




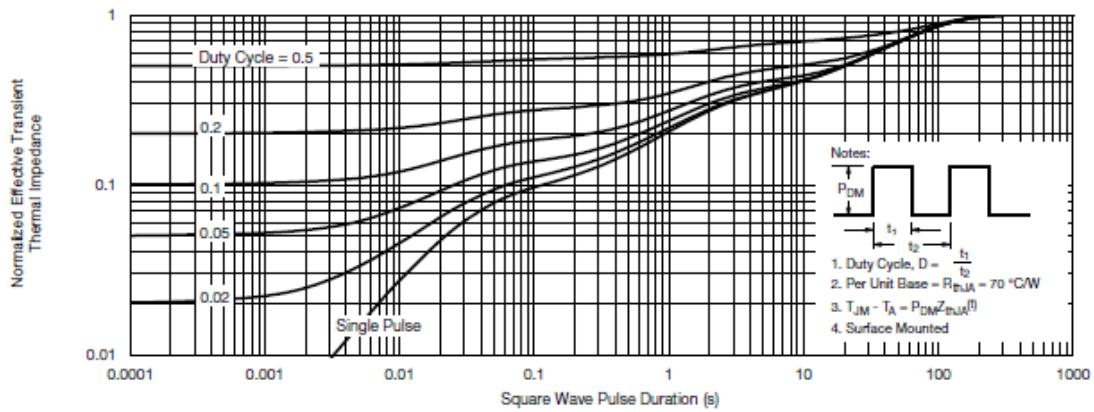
## Typical Characteristics



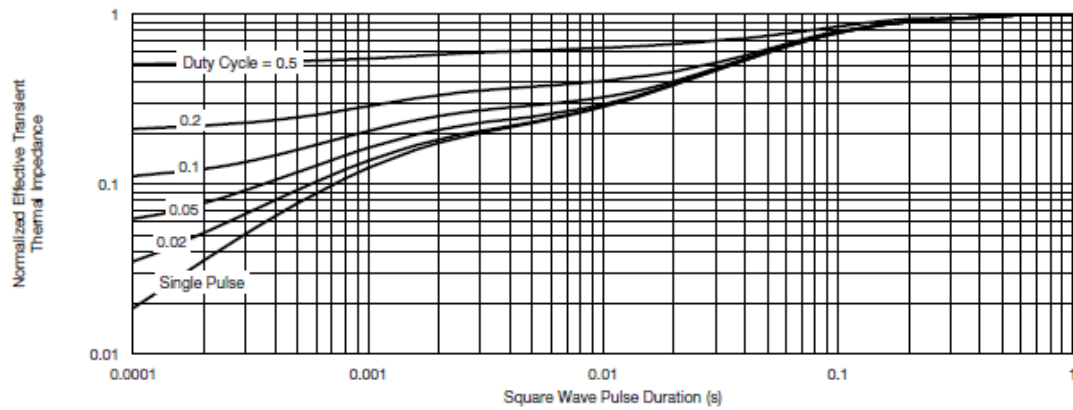
Power, Junction-to-Case



Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

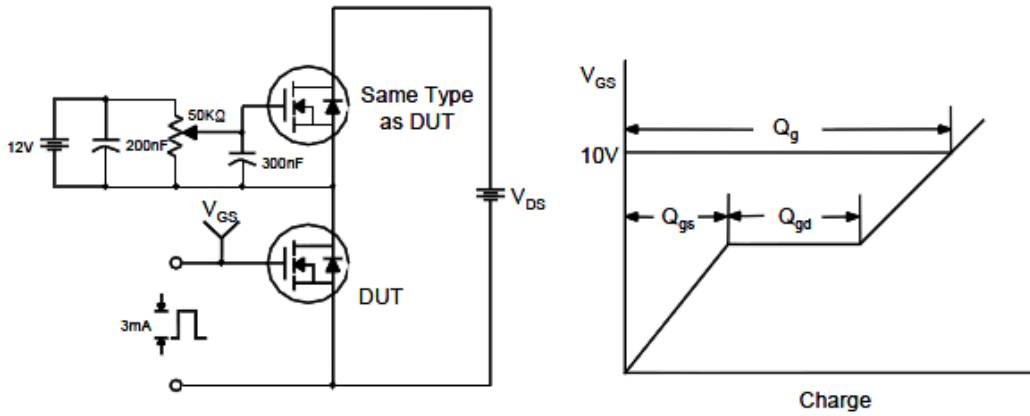


Normalized Thermal Transient Impedance, Junction-to-Case

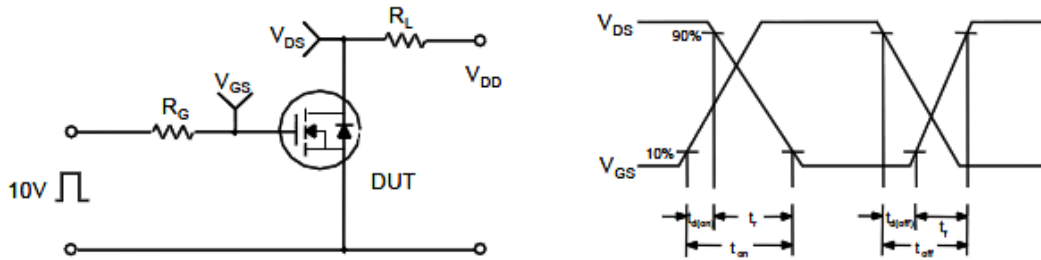


## Typical Characteristics

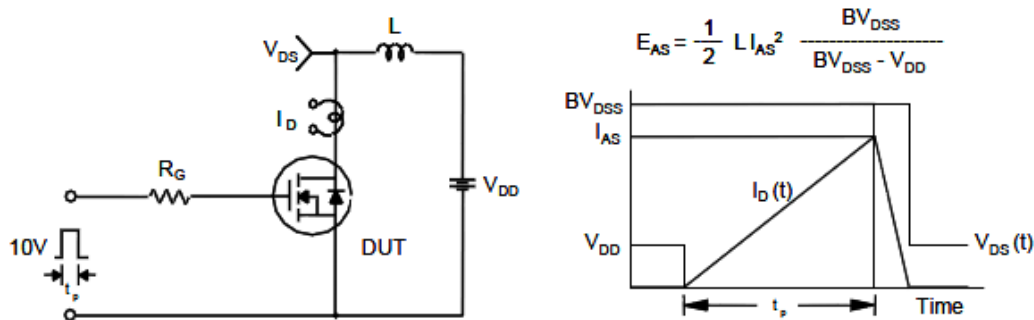
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

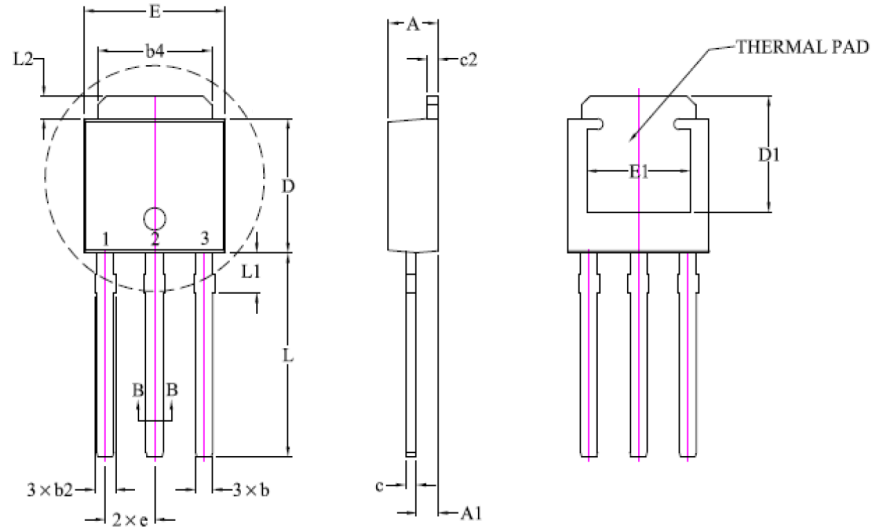


Unclamped Inductive Switching Test Circuit & Waveforms





**Package Information ( TO-251-3L )**



Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	2.220	2.420	0.087	0.095
A1	0.890	1.140	0.035	0.045
b	0.550	0.670	0.022	0.026
b1	0.550	0.650	0.022	0.025
b2	0.760	0.960	0.030	0.038
b4	5.200	5.400	0.205	0.213
c	0.460	0.570	0.018	0.023
c1	0.450	0.550	0.018	0.022
c2	0.450	0.550	0.018	0.022
D	5.950	6.250	0.234	0.246
D1	4.200	4.500	0.165	0.177
E	6.400	6.700	0.252	0.264
E1	4.750	4.850	0.187	0.191
e	2.28 REF		0.090 REF	
L	8.900	9.500	0.350	0.374
L1	1.900	2.290	0.075	0.090
L2	0.900	1.000	0.035	0.039

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