Alfa-MOS Technology

AFN6200S

100V N-Channel Enhancement Mode MOSFET

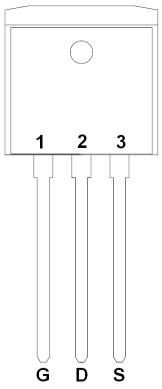
General Description

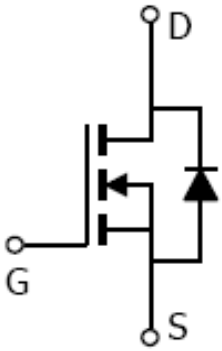
AFN6200S, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.
These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

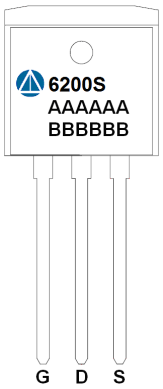
Features

- 100V/50A,$R_{DS(ON)}$=5.8mΩ@V_{GS}=10V
- 100V/20A,$R_{DS(ON)}$=8.5mΩ@V_{GS}=5V
- Super high density cell design for extremely low $R_{DS\ (ON)}$
- TO-262 package design

Pin Description (TO-262)







Application

- Power Supply - Secondary Synchronous Rectification
- Industrial
- Primary Switch

Pin Define

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN6200ST262TG	6200S AAAAAA BBBBBB	TO-262	Tube	50 EA

※ A Lot code
※ B Date code
※ AFN6200ST262TG : Tube ; Pb- Free ; Halogen –Free

Absolute Maximum Ratings

(T_A=25℃ Unless otherwise noted)

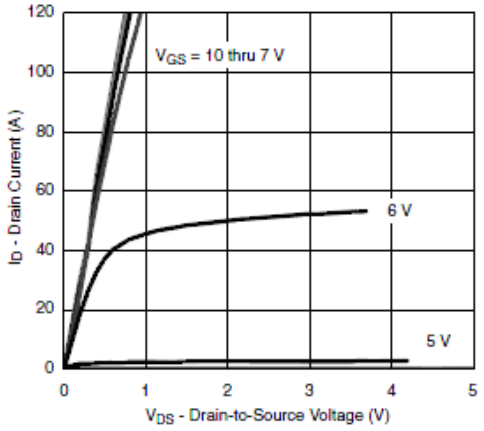
Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate –Source Voltage		V_{GSS}	±20	V
Continuous Drain Current(T_J=150℃)	T_c=25℃	I_D	120	A
	T_c=70℃		90	
Pulsed Drain Current		I_{DM}	400	
Continuous Source Current(Diode Conduction)		I_S	100	
Single Pulse Avalanche Current		I_{AS}	70	
Power Dissipation	T_A=25℃	P_D	75	W
Operating Junction Temperature		T_J	150	℃
Storage Temperature Range		T_{STG}	-55/150	℃
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	62.5	℃/W

Electrical Characteristics

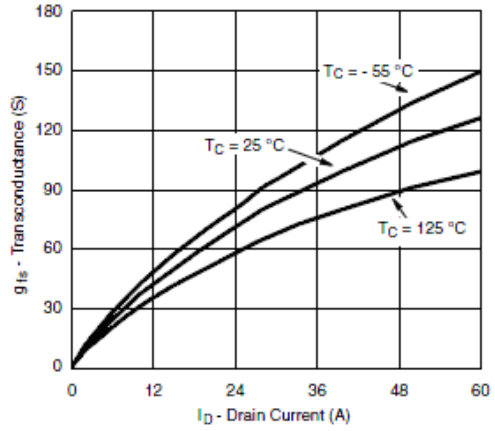
(T_A=25℃ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V,I_D=250uA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	2.0		4.0	
Gate Leakage Current	I_{GSS}	V_{DS}=0V,V_{GS}=±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V,V_{GS}=0V			1	uA
		V_{DS}=80V,V_{GS}=0V T_J=85℃			30	
On-State Drain Current	$I_{D(on)}$	V_{DS}≧10V,V_{GS}=10V	70			A
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=50A		4.6	5.8	mΩ
		V_{GS}=5V,I_D=20A		6.5	8.5	
Forward Transconductance	g_{FS}	V_{DS}=15V,I_D=20A		62		S
Diode Forward Voltage	V_{SD}	I_S=30A,V_{GS}=0V		0.8	1.3	V
Dynamic						
Total Gate Charge	Q_g	V_{DS}=50V,V_{GS}=10V I_D=85A		95	115	nC
Gate-Source Charge	Q_{gs}			45		
Gate-Drain Charge	Q_{gd}			35		
Input Capacitance	C_{iss}	V_{DS}=50V,V_{GS}=0V f=1MHz		6250		pF
Output Capacitance	C_{oss}			580		
Reverse Transfer Capacitance	C_{rss}			235		
Turn-On Time	$t_{d(on)}$	V_{DD}=50V,R_L=0.6Ω I_D=85A,V_{GEN}=10V R_G=1Ω		32	65	ns
	t_r			25	55	
Turn-Off Time	$t_{d(off)}$			40	80	
	t_f			15	40	

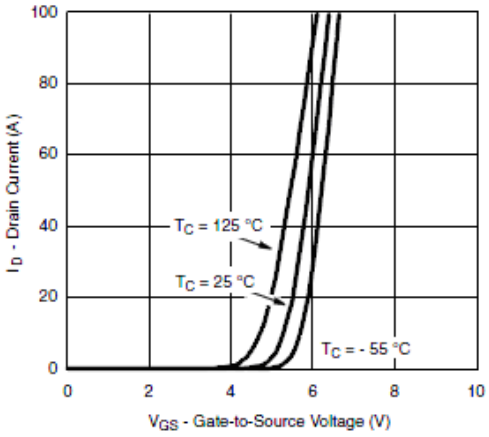
Typical Characteristics



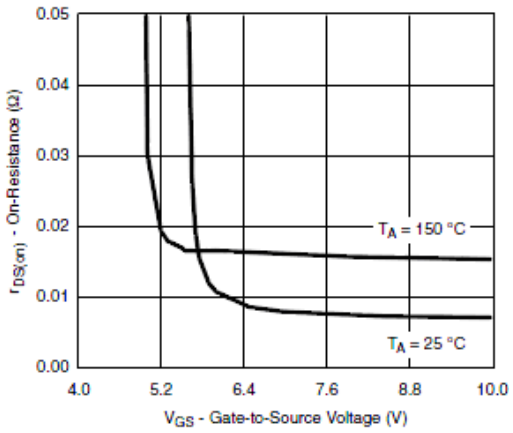
Output Characteristics



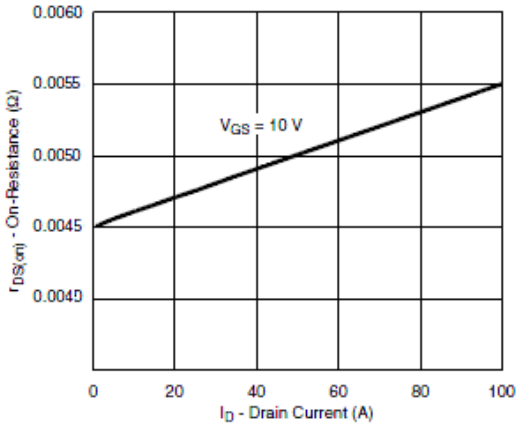
Transconductance



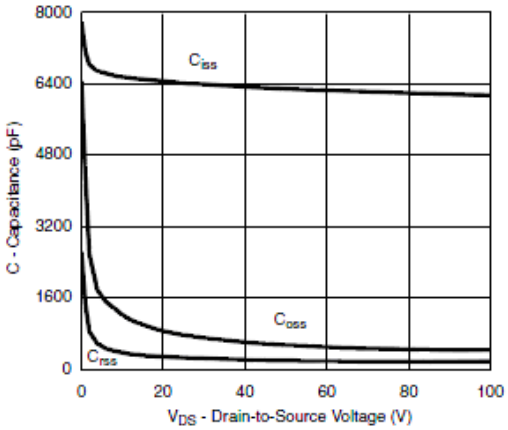
Transfer Characteristics



On-resistance vs. Gate-to-Source Voltage

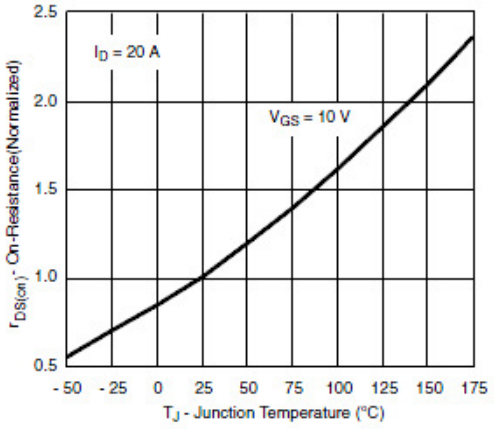


On-Resistance vs. Drain Current

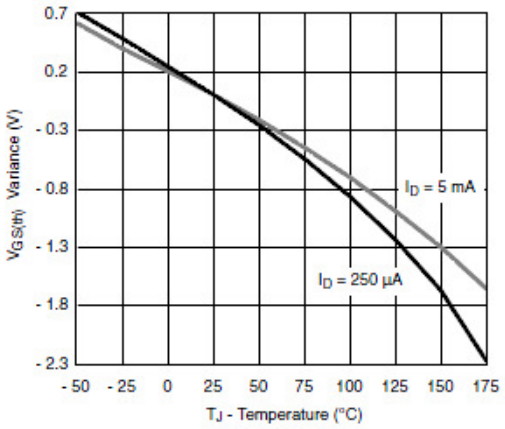


Capacitance

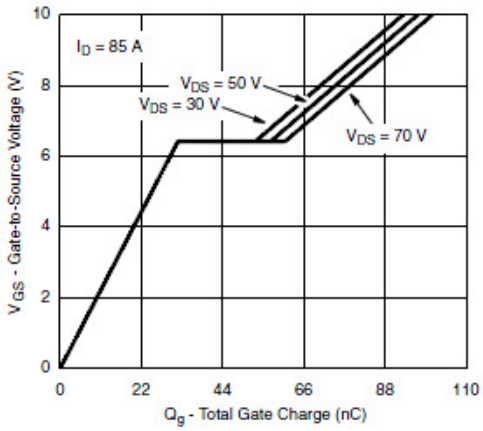
Typical Characteristics



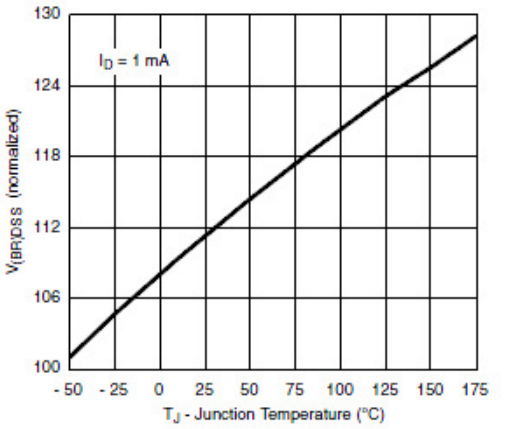
On-Resistance vs. Junction Temperature



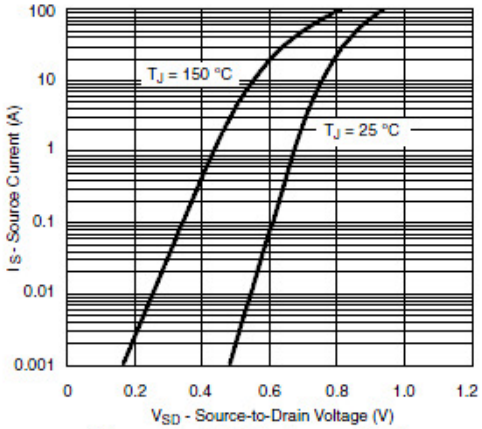
Threshold Voltage



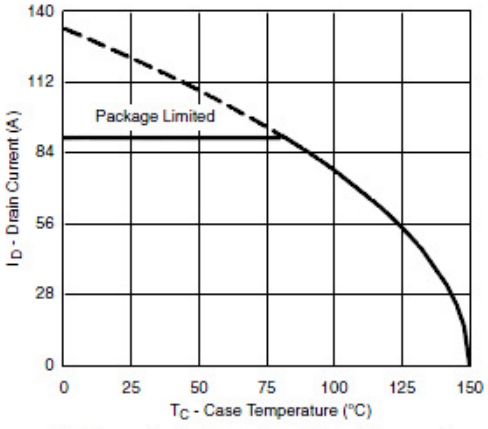
Gate Charge



Drain Source Breakdown vs. Junction Temperature

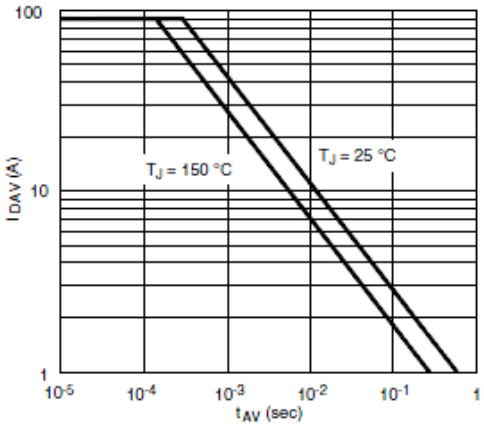


Source-Drain Diode Forward Voltage

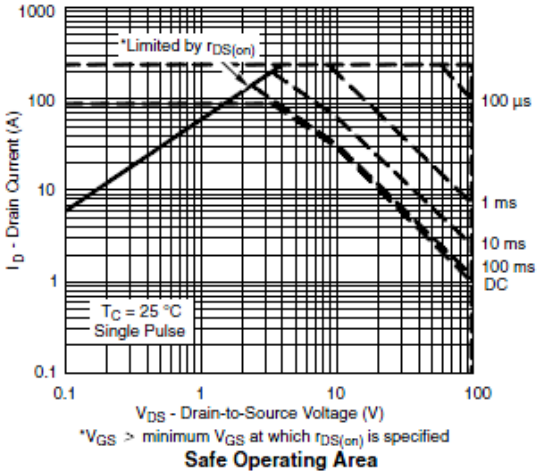


Maximum Drain Current vs. Case Temperature

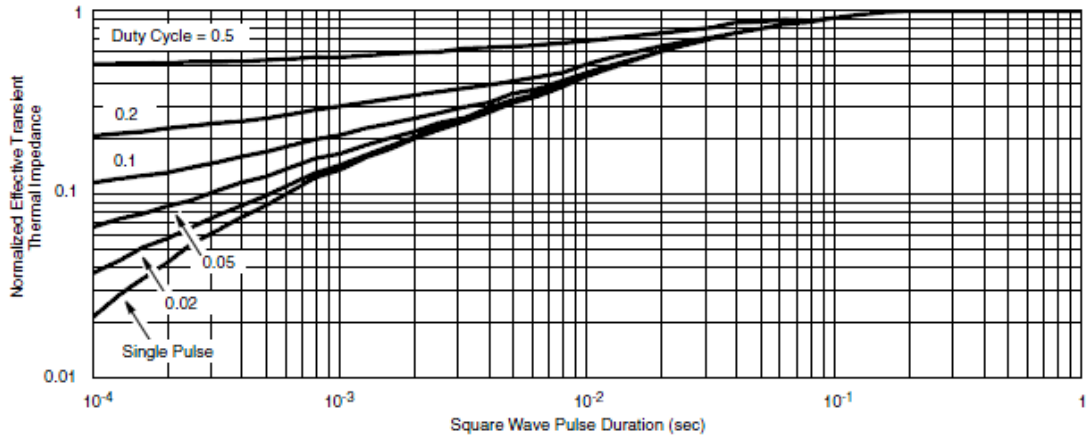
Typical Characteristics



Single Pulse Avalanche Current Capability vs. Time



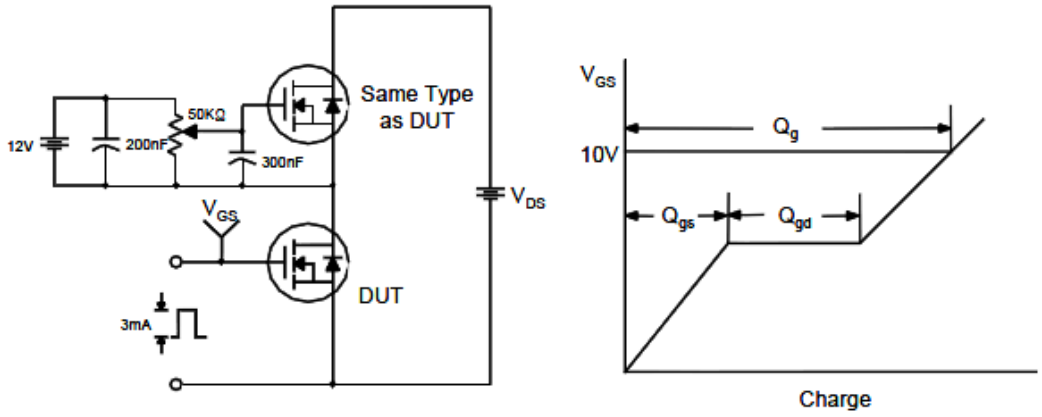
Safe Operating Area



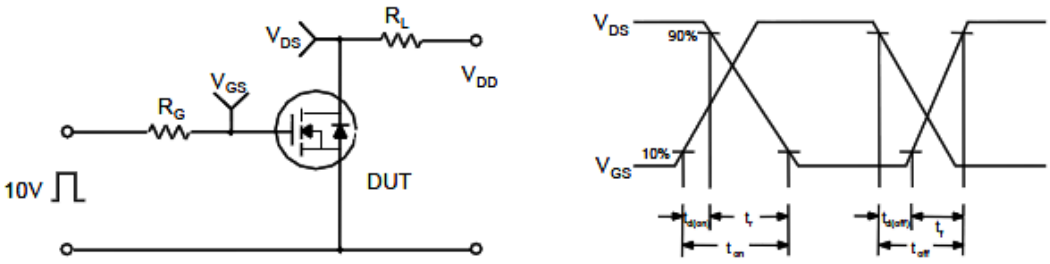
Normalized Thermal Transient Impedance, Junction-to-Case

Typical Characteristics

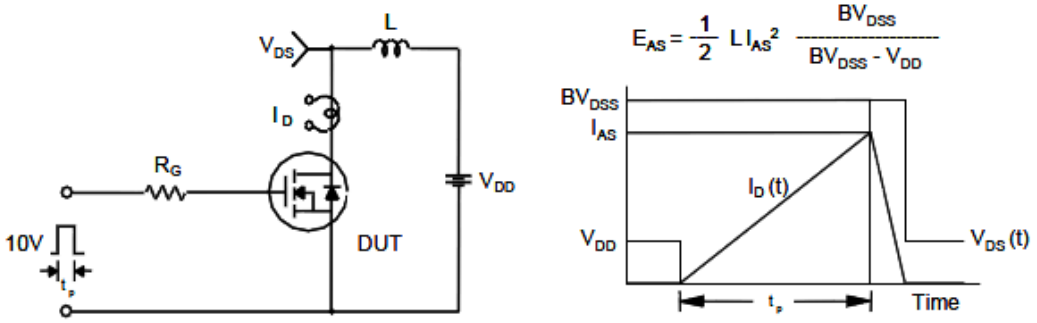
Gate Charge Test Circuit & Waveform



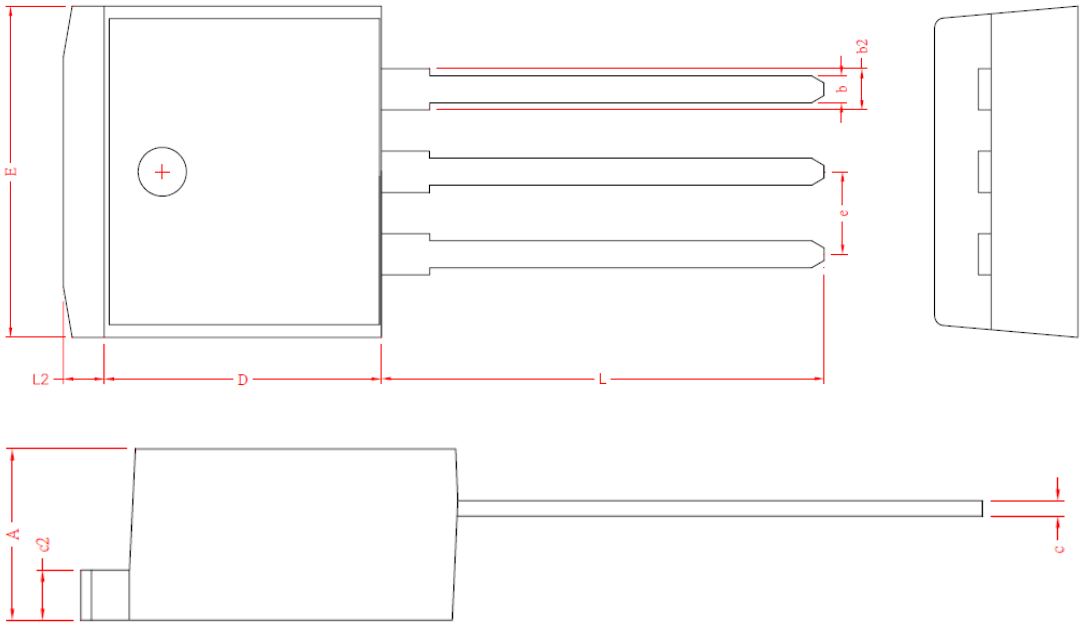
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Package Information (TO-262)



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.80	c2	1.25	1.45
b	0.76	1.0	b2	1.17	1.47
D	8.6	9.0	L	13.25	14.25
c	0.36	0.50	e	2.54 REF.	
E	9.80	10.4	L2	1.27 REF.	

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