



**Alfa-MOS
Technology**

**AFN8968
100V N-Channel
Enhancement Mode MOSFET**

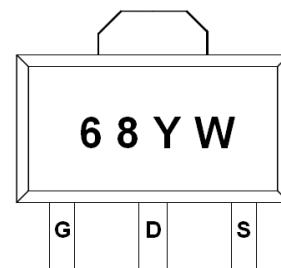
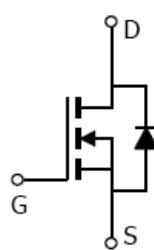
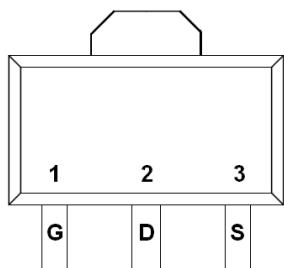
General Description

AFN8968, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- 100V/3.0A, $R_{DS(ON)}=300m\Omega @ V_{GS}=10V$
- 100V/2.0A, $R_{DS(ON)}=310m\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- SOT-89-3L package design

Pin Description (SOT-89-3L)



Application

- Motor and Load Control
- Power Management in White LED System
- Push Pull Converter
- LCD TV Inverter & AD/DC Inverter Systems.

Pin Define

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN8968S89RG	68YW	SOT-89-3L	Tape & Reel	1000 EA

- ※ 68 parts code
- ※ Y year code (0 ~ 9)
- ※ W week code (A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52)
- ※ AFN8968S89RG : 7" Tape & Reel ; Pb-Free ; Halogen-Free



Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	100	V
Gate –Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	3.0	A
		2.0	
Pulsed Drain Current	I_{DM}	6	A
Continuous Source Current(Diode Conduction)	I_S	1.6	A
Power Dissipation	P_D	1.45	W
		0.6	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ\text{C}/\text{W}$

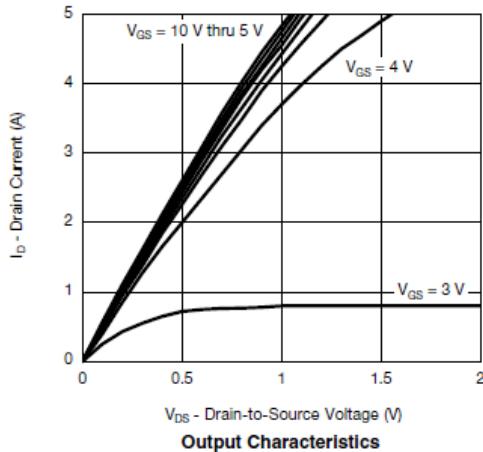
Electrical Characteristics

($T_A=25^\circ\text{C}$ Unless otherwise noted)

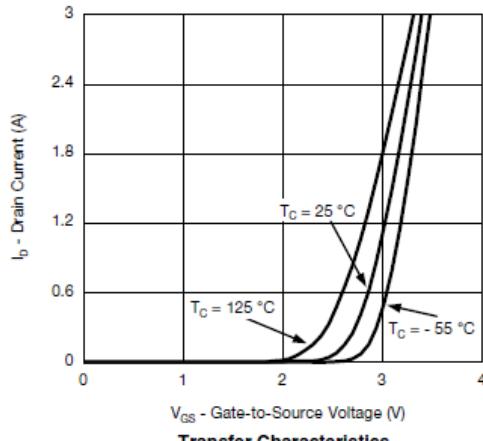
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	100			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		2.0	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=80\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=80\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS}\geq 5\text{V}, V_{GS}=4.5\text{V}$	5			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=3.0\text{A}$	280	300		m Ω
		$V_{GS}=4.5\text{V}, I_D=2.0\text{A}$	290	310		
Forward Transconductance	g_{FS}	$V_{DS}=20\text{V}, I_D=1.5\text{A}$		2		S
Diode Forward Voltage	V_{SD}	$I_S=1.3\text{A}, V_{GS}=0\text{V}$		0.85	1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=50\text{V}, V_{GS}=4.5\text{V}$ $I_D\equiv 1.6\text{A}$		2.8	5.8	nC
Gate-Source Charge	Q_{gs}			0.75		
Gate-Drain Charge	Q_{gd}			1.4		
Input Capacitance	C_{iss}	$V_{DS}=50\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		200		pF
Output Capacitance	C_{oss}			22		
Reverse Transfer Capacitance	C_{rss}			13		
Turn-On Time	$t_{d(on)}$	$V_{DD}=50\text{V}, R_L=39\Omega$ $I_D\equiv 1.3\text{A}, V_{GEN}=4.5\text{V}$ $R_G=1\Omega$		25	50	ns
	t_r			20	50	
Turn-Off Time	$t_{d(off)}$			15	30	
	t_f			10	25	



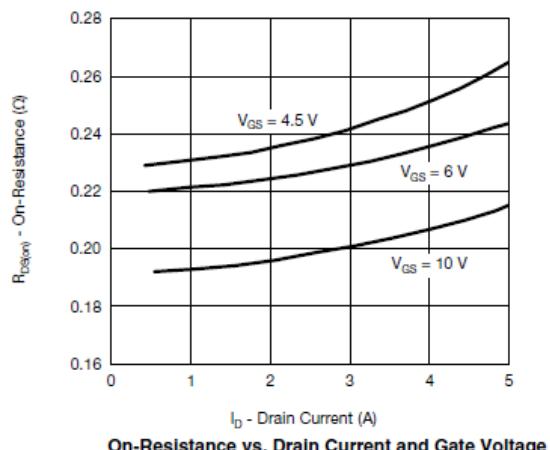
Typical Characteristics



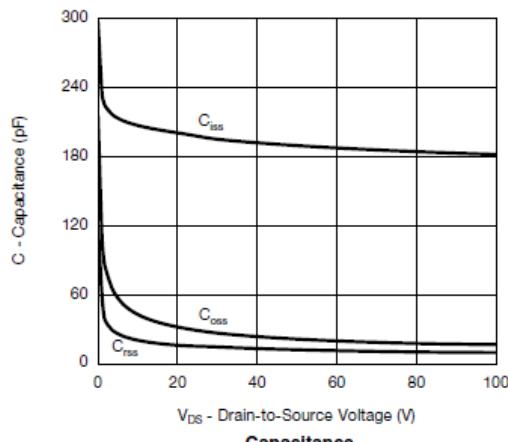
Output Characteristics



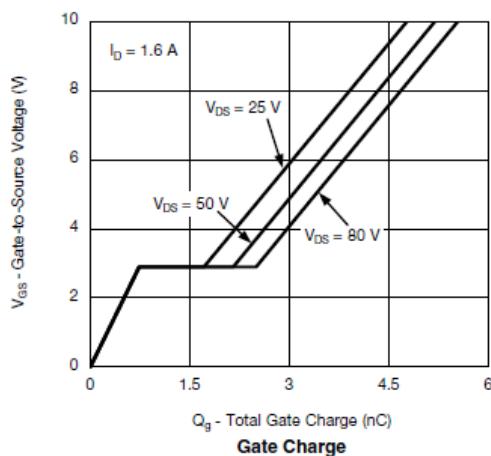
Transfer Characteristics



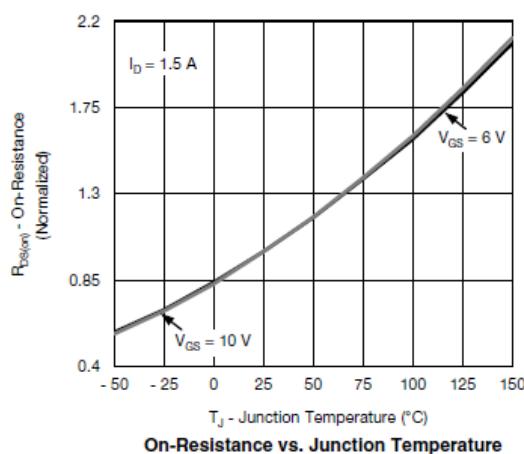
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge



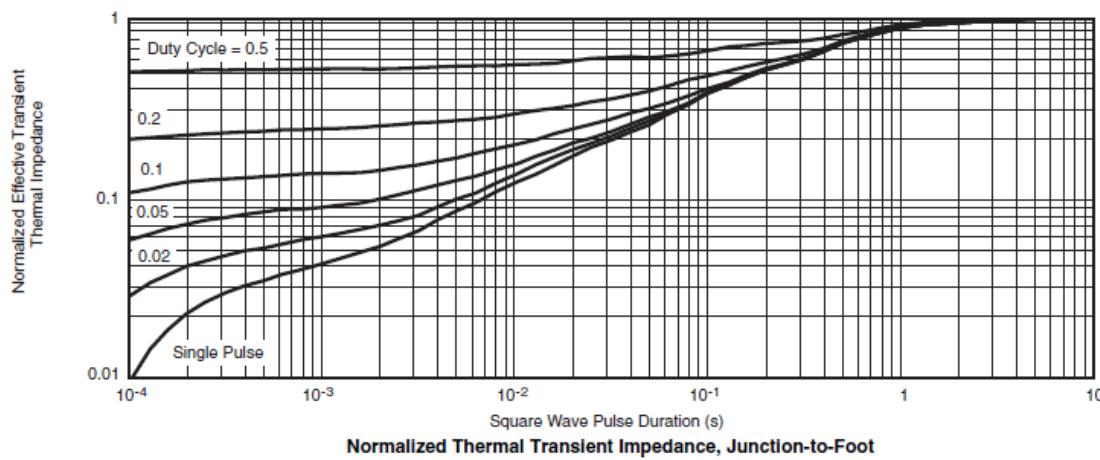
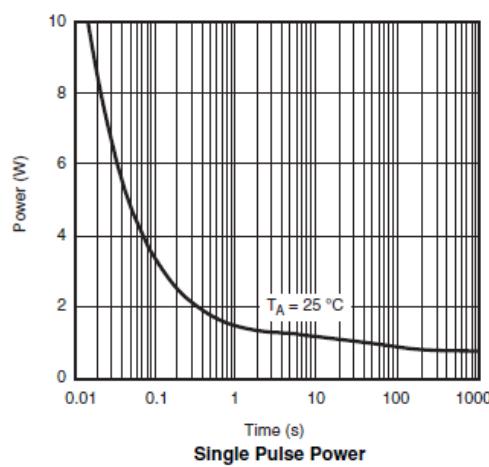
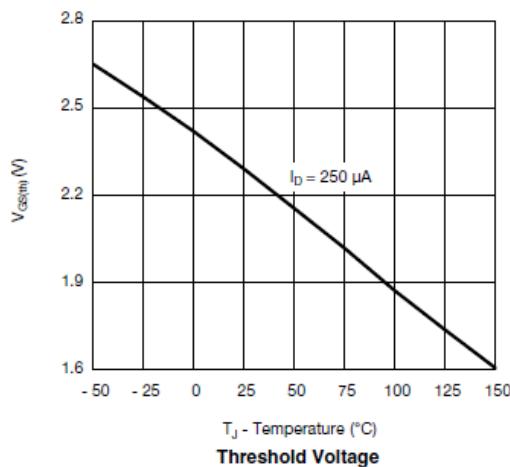
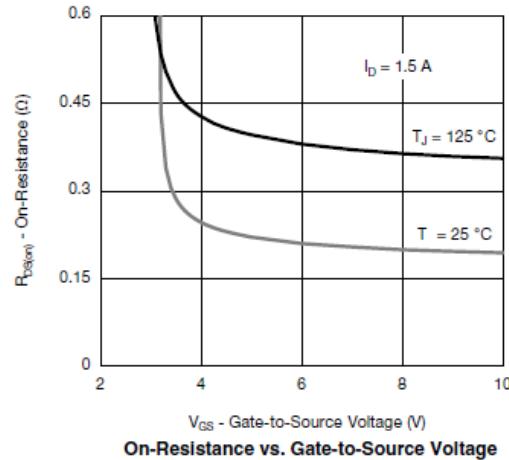
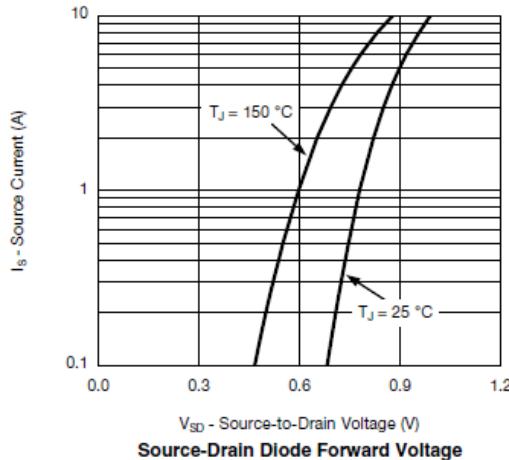
On-Resistance vs. Junction Temperature



**Alfa-MOS
Technology**

**AFN8968
100V N-Channel
Enhancement Mode MOSFET**

Typical Characteristics



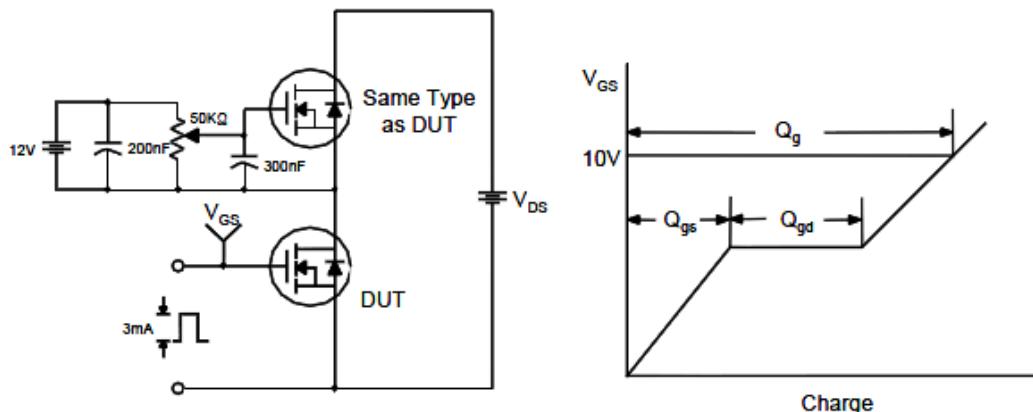


**Alfa-MOS
Technology**

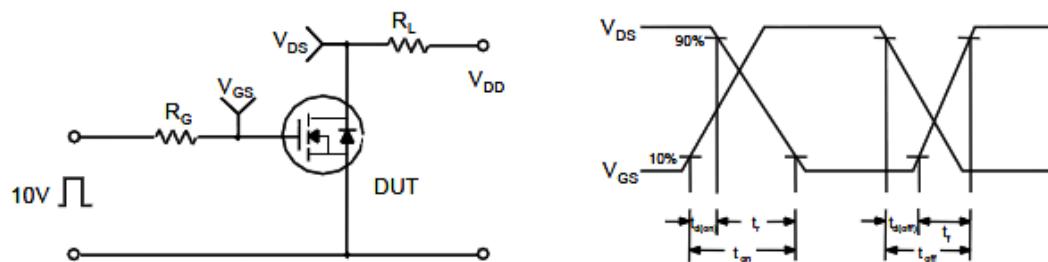
**AFN8968
100V N-Channel
Enhancement Mode MOSFET**

Typical Characteristics

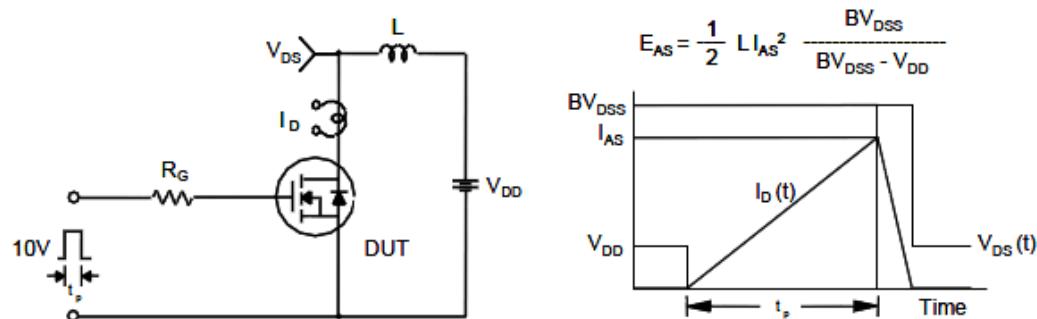
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

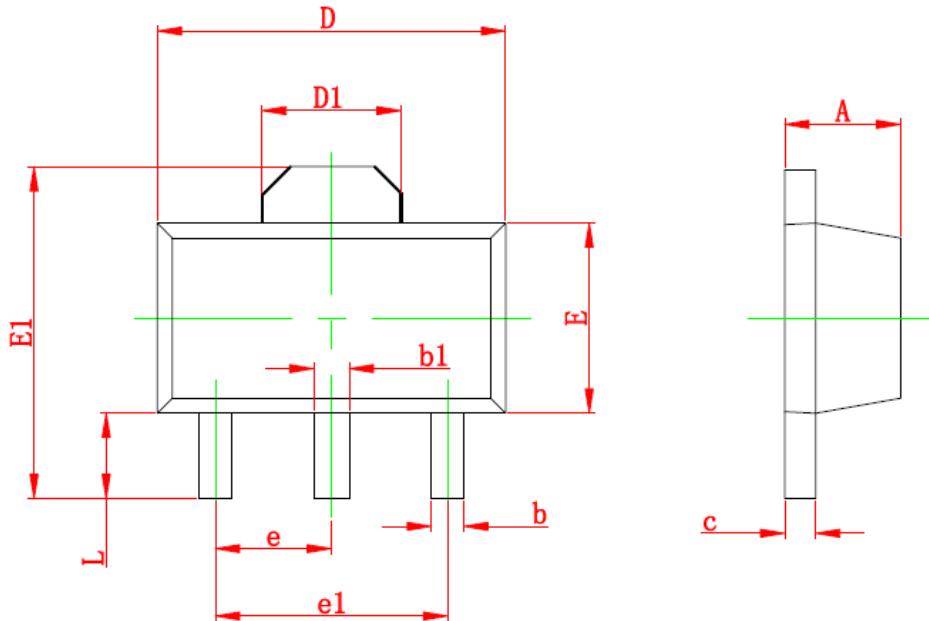


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (SOT-89-3L)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
L	0.900	1.200	0.035	0.047

©2010 Alfa-MOS Technology Corp.
2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)
Tel : 886 2) 2651 3928
Fax : 886 2) 2786 8483
©<http://www.alfa-mos.com>