



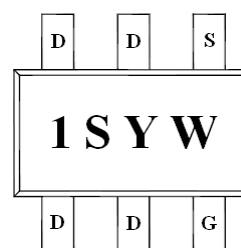
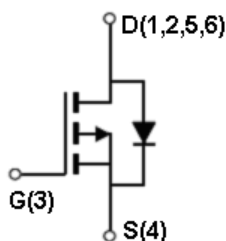
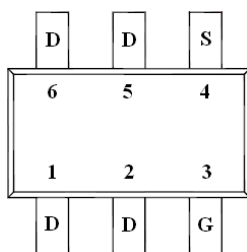
General Description

AFP3481S, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- -30V/-5.4A, $R_{DS(ON)}=62m\Omega@V_{GS}=-10.0V$
- -30V/-4.2A, $R_{DS(ON)}=90m\Omega@V_{GS}=-4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- TSOT-23-6L package design

Pin Description (TSOT-23-6L)



Application

- Power Management in Note book
- LED Display
- DC-DC System
- LCD Panel

Pin Define

Pin	Symbol	Description
1	D	Drain
2	D	Drain
3	G	Gate
4	S	Source
5	D	Drain
6	D	Drain

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFP3481STS26RG	1SYW	TSOT-23-6L	Tape & Reel	3000 EA

- ※ 1S parts code
- ※ Y year code (0 ~ 9)
- ※ W week code (A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52)
- ※ AFP3481STS26RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings

(T_A=25°C Unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	-30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	I _D	T _A =25°C	-5.4
		T _A =70°C	-4.2
Pulsed Drain Current	I _{DM}	-20	A
Continuous Source Current(Diode Conduction)	I _S	-1.7	A
Power Dissipation	P _D	T _A =25°C	2.0
		T _A =70°C	1.3
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	120	°C/W

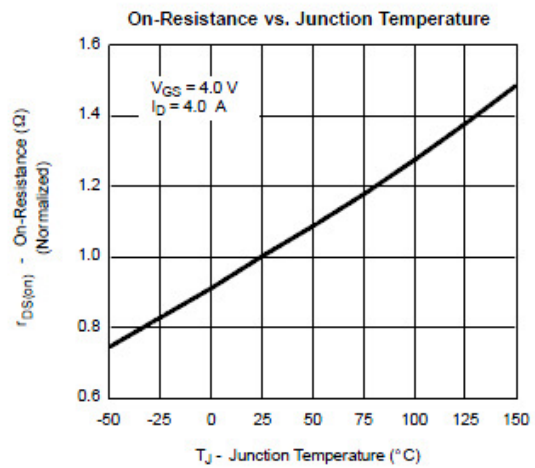
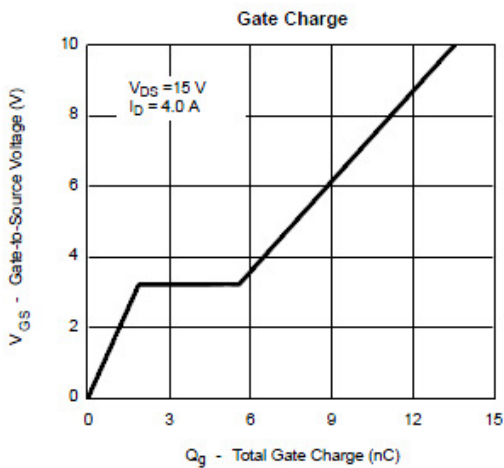
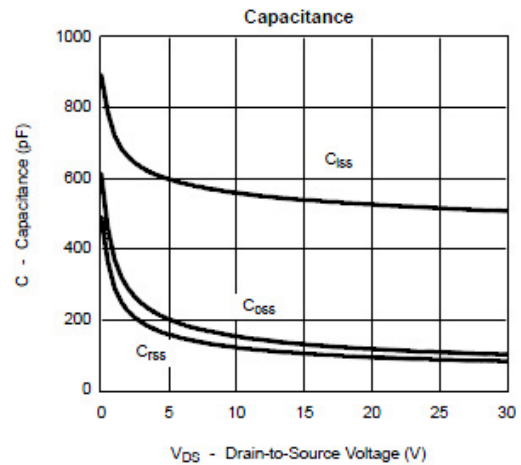
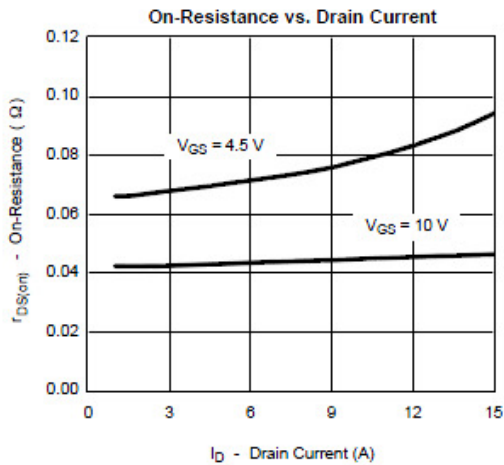
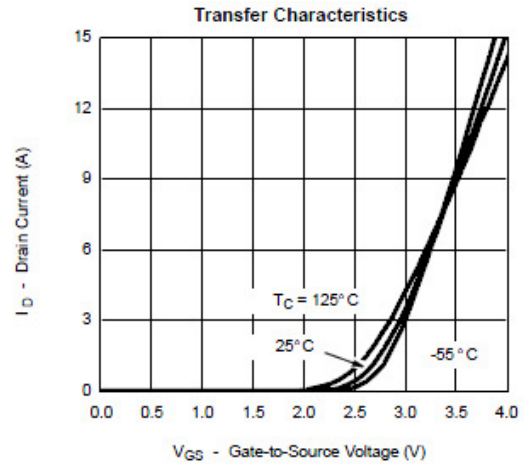
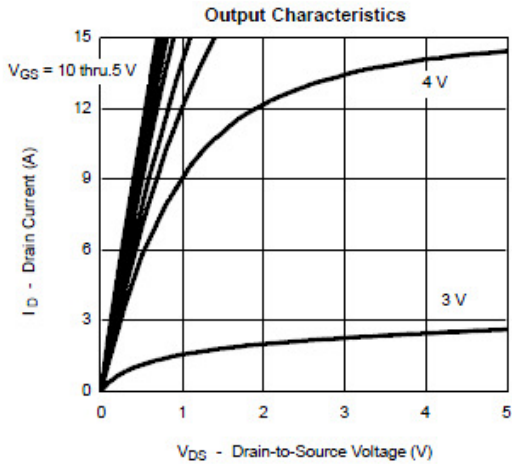
Electrical Characteristics

(T_A=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250uA	-30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1.0		-2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	uA
		V _{DS} =-24V, V _{GS} =0V T _A =85°C			-30	
On-State Drain Current	I _{D(on)}	V _{DS} ≤ -5V, V _{GS} =-10V	-10			A
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-10.0V, I _D =-5.4A		55	62	mΩ
		V _{GS} =-4.5V, I _D =-4.2A		75	90	
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-4.0A		10		S
Diode Forward Voltage	V _{SD}	I _S =-1.7A, V _{GS} =0V		-0.7	-1.3	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-15V, V _{GS} =-10V I _D ≡-3.5A		10	18	nC
Gate-Source Charge	Q _{gs}			1.6		
Gate-Drain Charge	Q _{gd}			3.0		
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V f=1MHz		450		pF
Output Capacitance	C _{oss}			95		
Reverse Transfer Capacitance	C _{rss}			55		
Turn-On Time	t _{d(on)}	V _{DD} =-15V, R _L =15Ω I _D ≡-1.0A, V _{GEN} =-10V R _G =6Ω		8	18	ns
	t _r			8	18	
Turn-Off Time	t _{d(off)}			25	50	
	t _f			25	35	

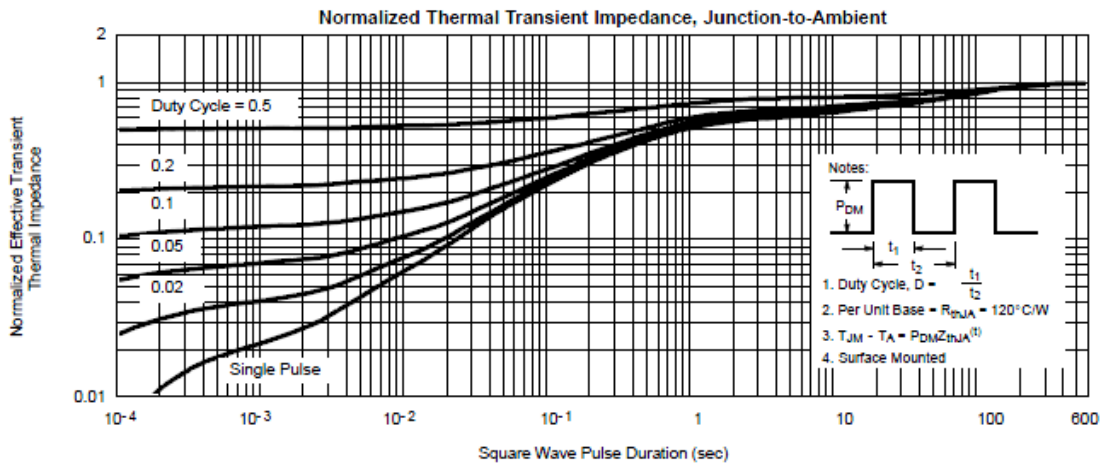
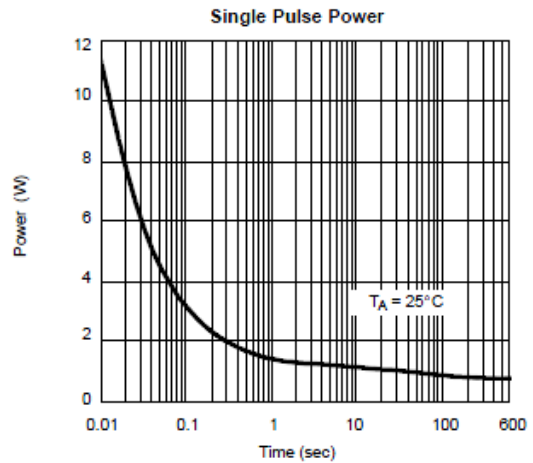
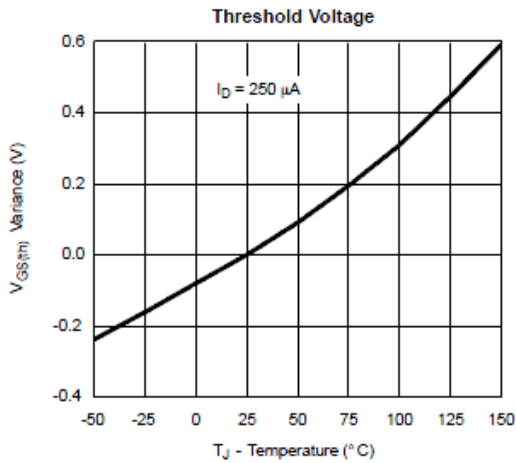
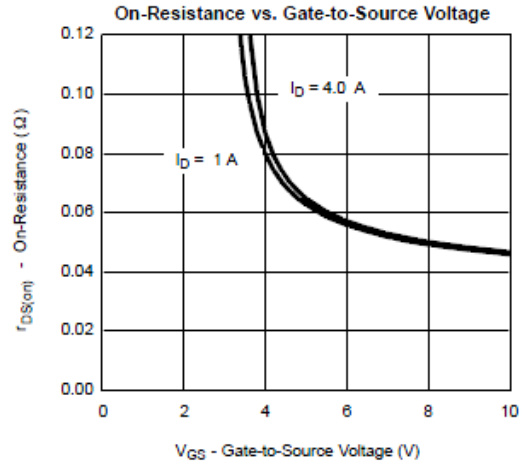
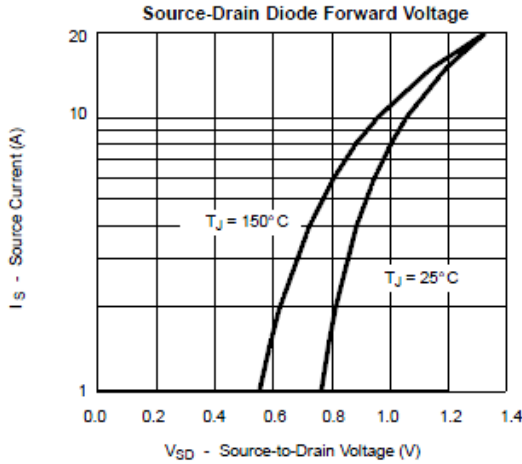


Typical Characteristics





Typical Characteristics





Typical Characteristics

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

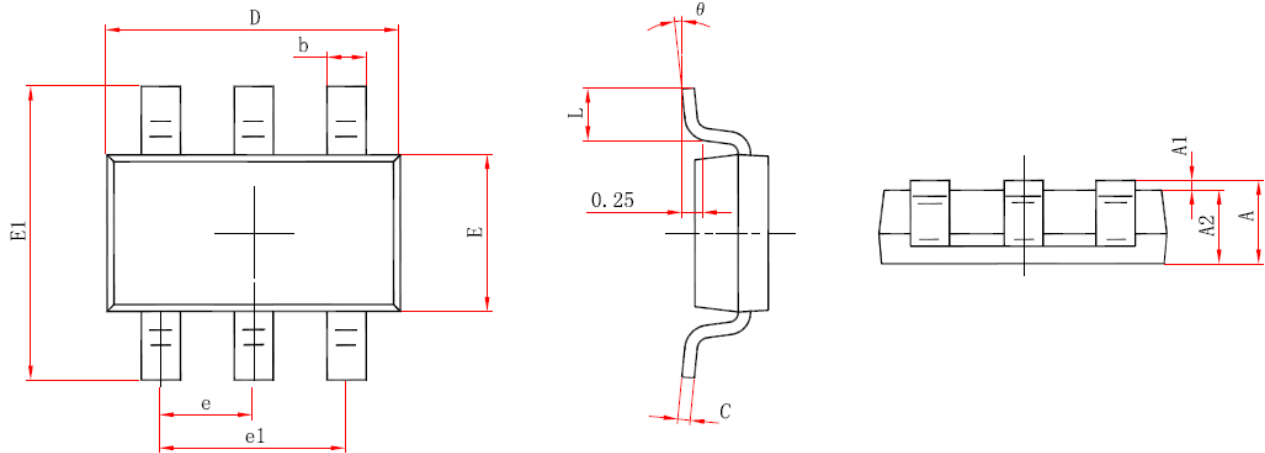


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (TSOT-23-6L)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b	0.350	0.500	0.014	0.020
c	0.080	0.200	0.003	0.008
D	2.820	3.020	0.111	0.119
E	1.600	1.700	0.063	0.067
E1	2.650	2.950	0.104	0.116
e	0.95 (BSC)		0.037(BSC)	
e1	1.90 (BSC)		0.075(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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